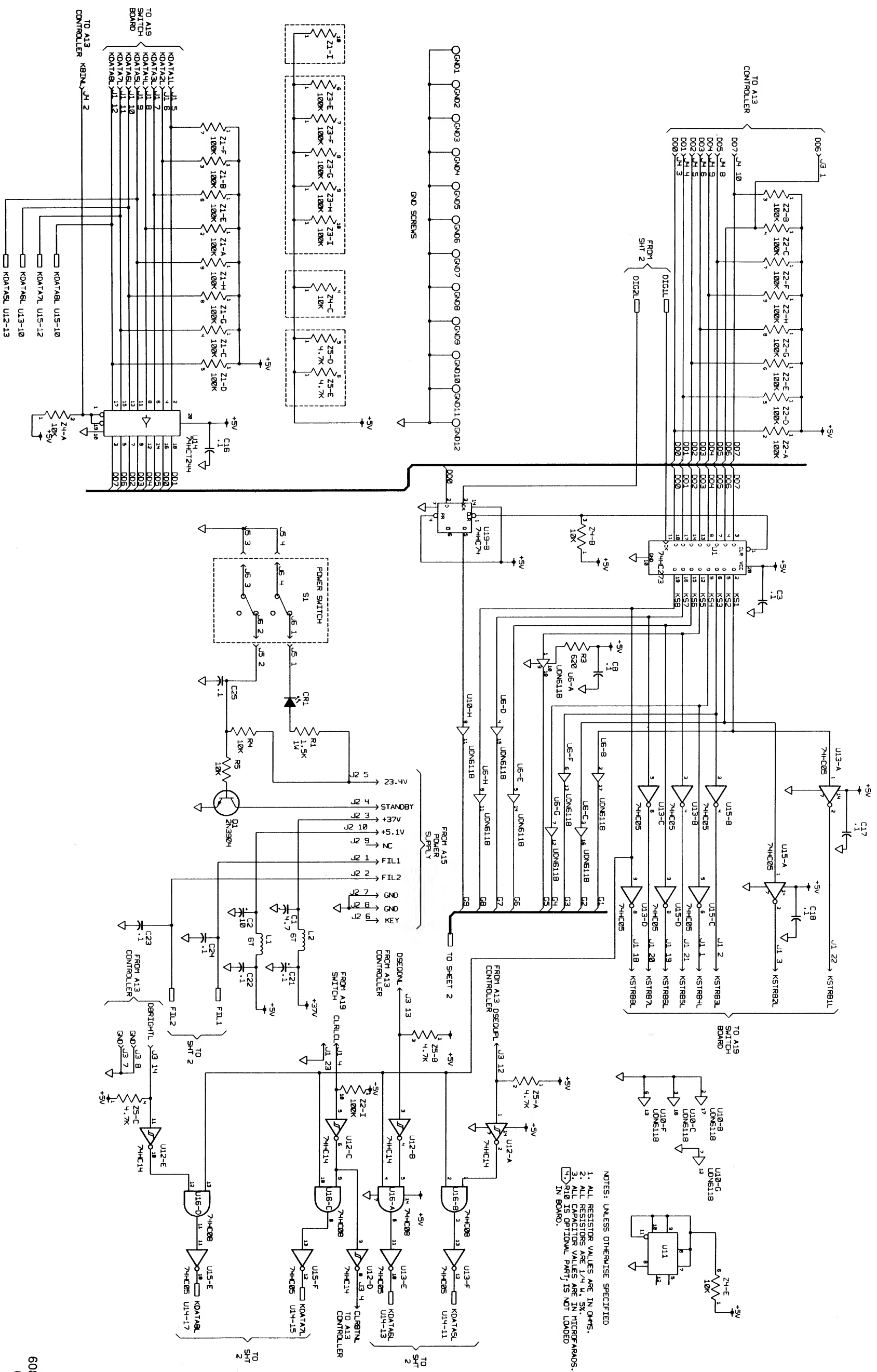


6080A-1604

Figure 8-1. A1 Display PCA

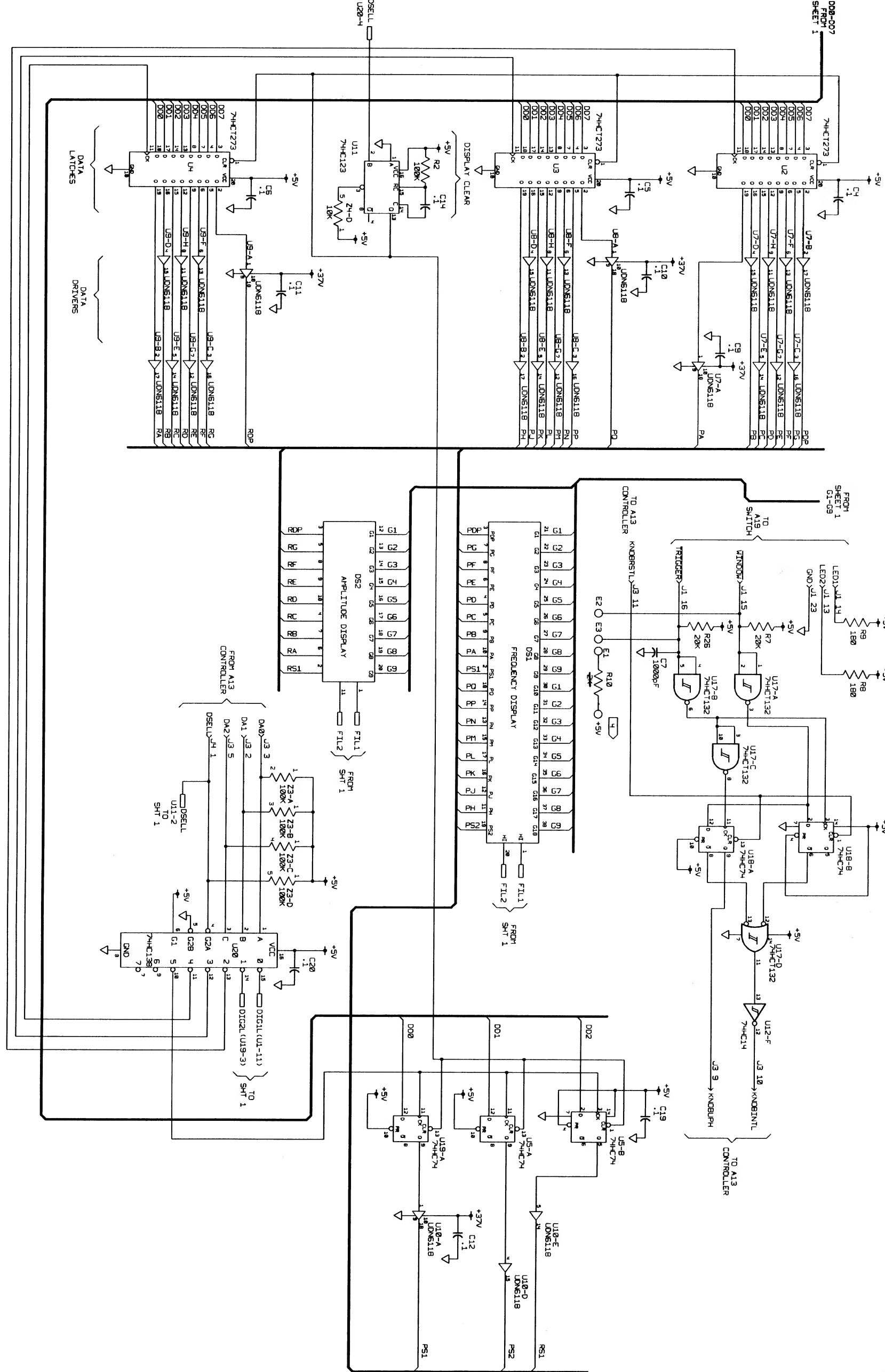
SCHEMATIC DIAGRAMS



NOTES: UNLESS OTHERWISE SPECIFIED

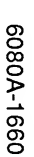
1. ALL RESISTOR VALUES ARE IN OHMS.
2. ALL RESISTORS ARE 1/4 W, 5%.
3. ALL CAPACITOR VALUES ARE IN MICROFARADS.
4. R10 IS OPTIONAL PART, IS NOT LOADED IN BOARD.

Figure 8-1. A1 Display PCA (cont)



6080A-1050
(2 of 2)

Figure 8-1. A1 Display PCA (cont)



8-6

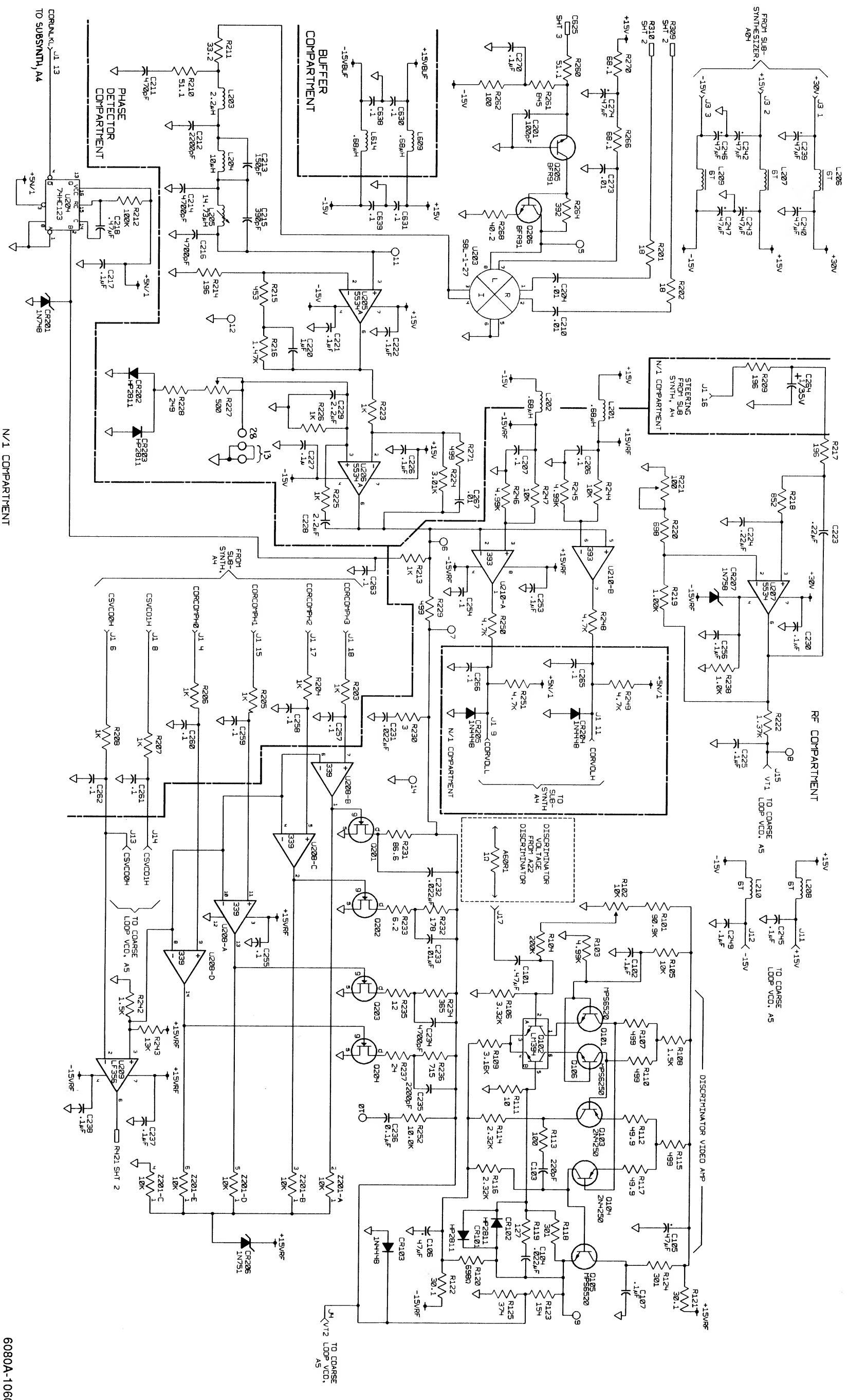


Figure 8-2. A2 Coarse Loop PCA (cont)

6080A-1060
(1 of 4)

SCHEMATIC DIAGRAMS

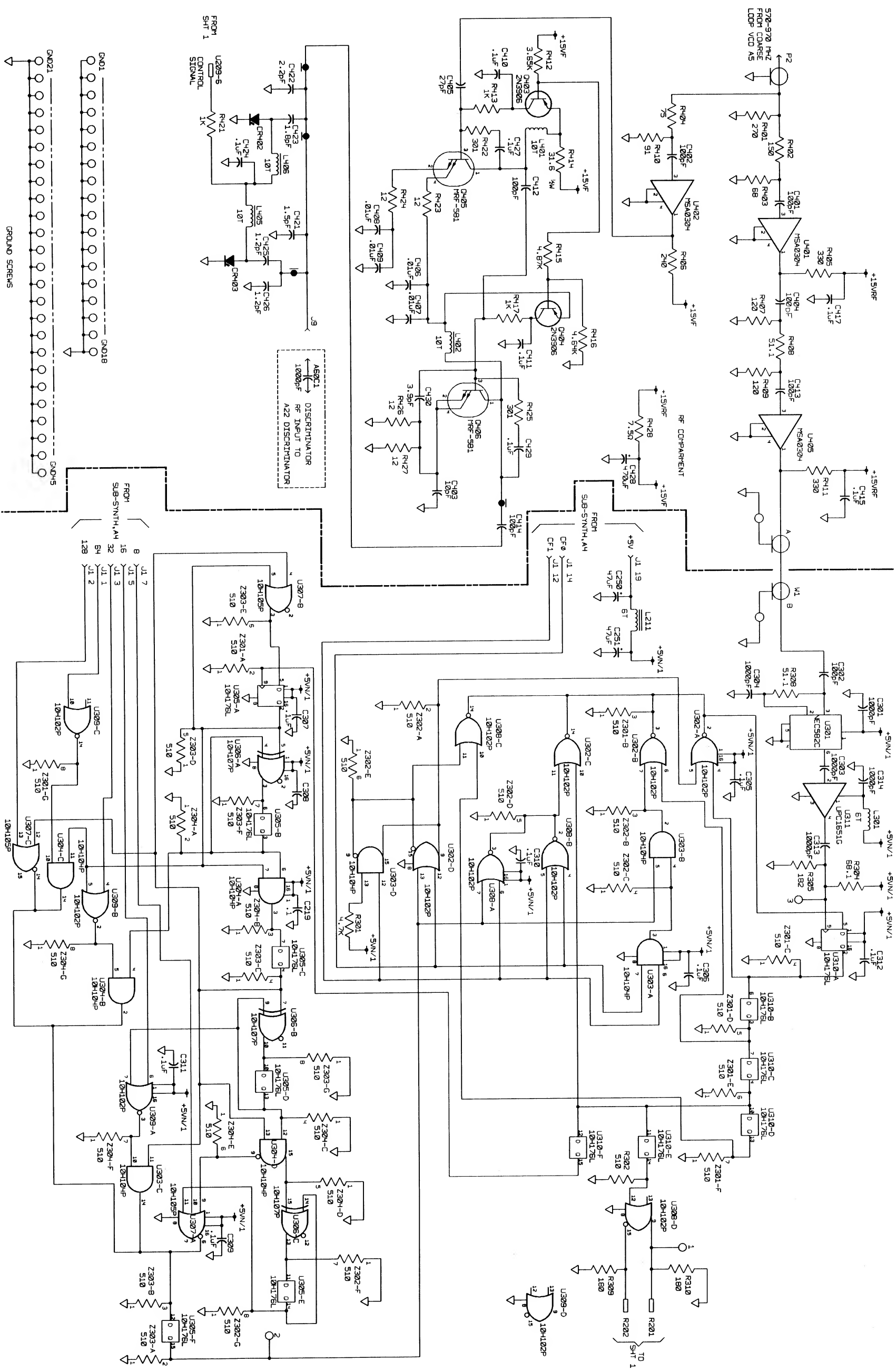


Figure 8-2. A2 Coarse Loop PCA (cont)

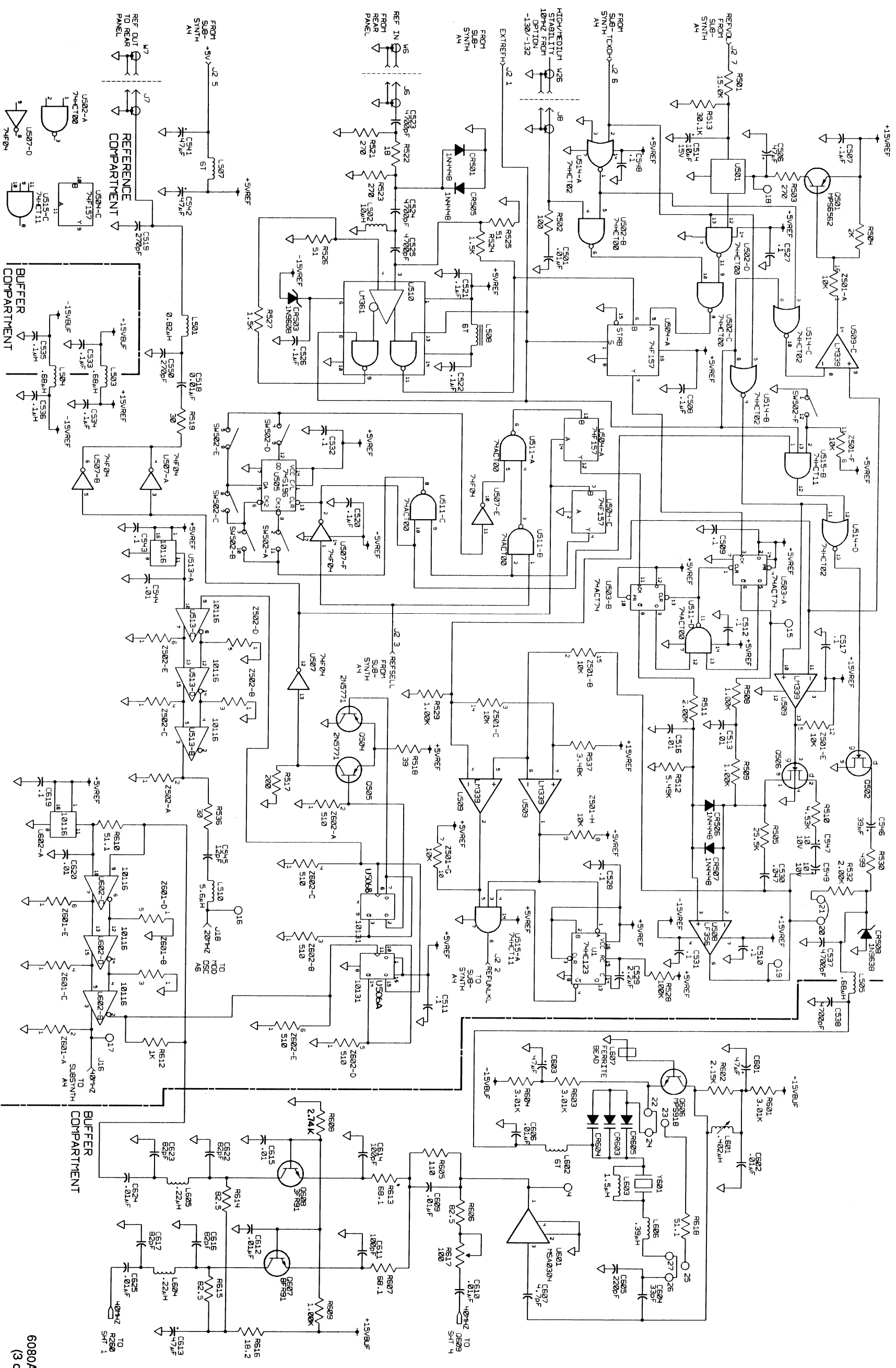


Figure 8-2. A2 Coarse Loop PCA (cont)

6080A-1060
(3 of 4)

SCHEMATIC DIAGRAMS

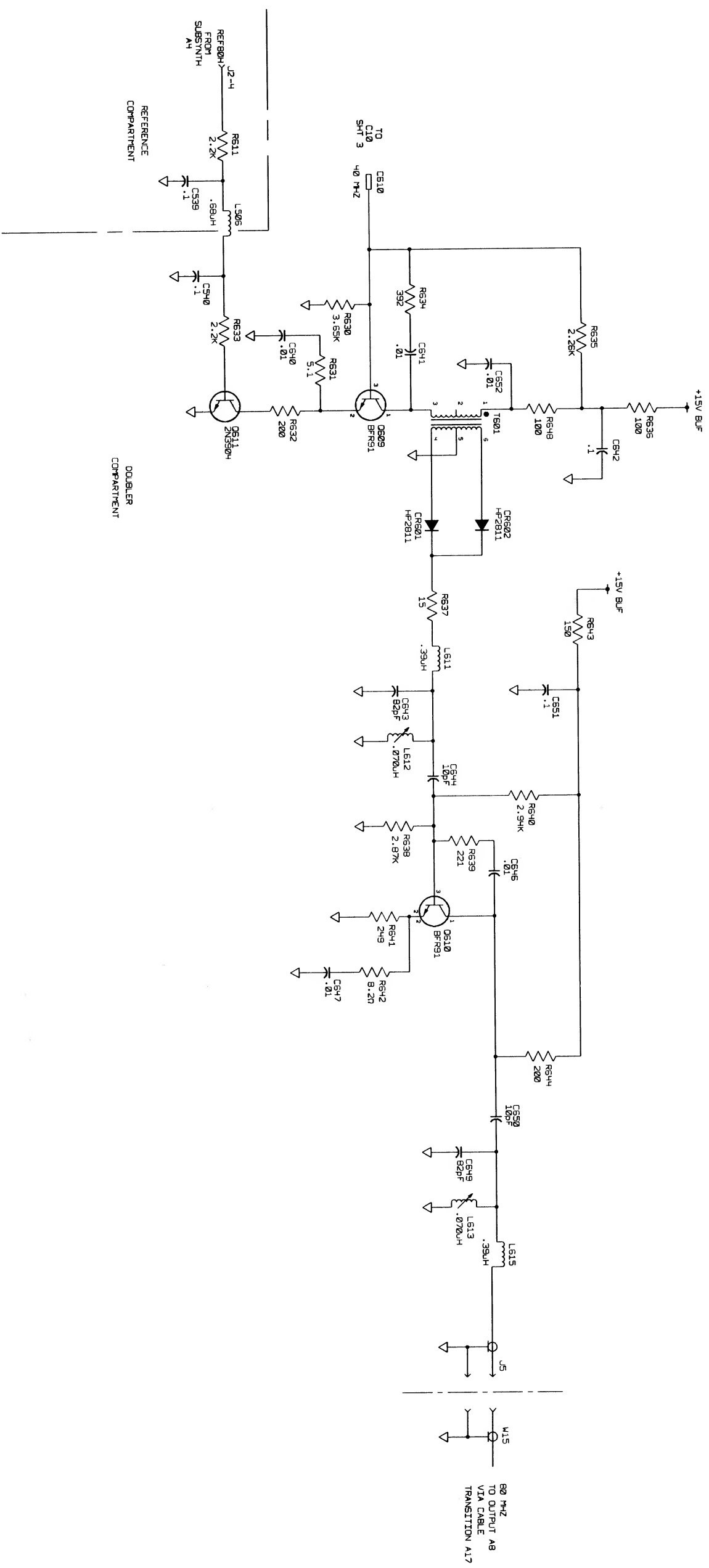


Figure 8-2. A2 Coarse Loop PCA (cont)

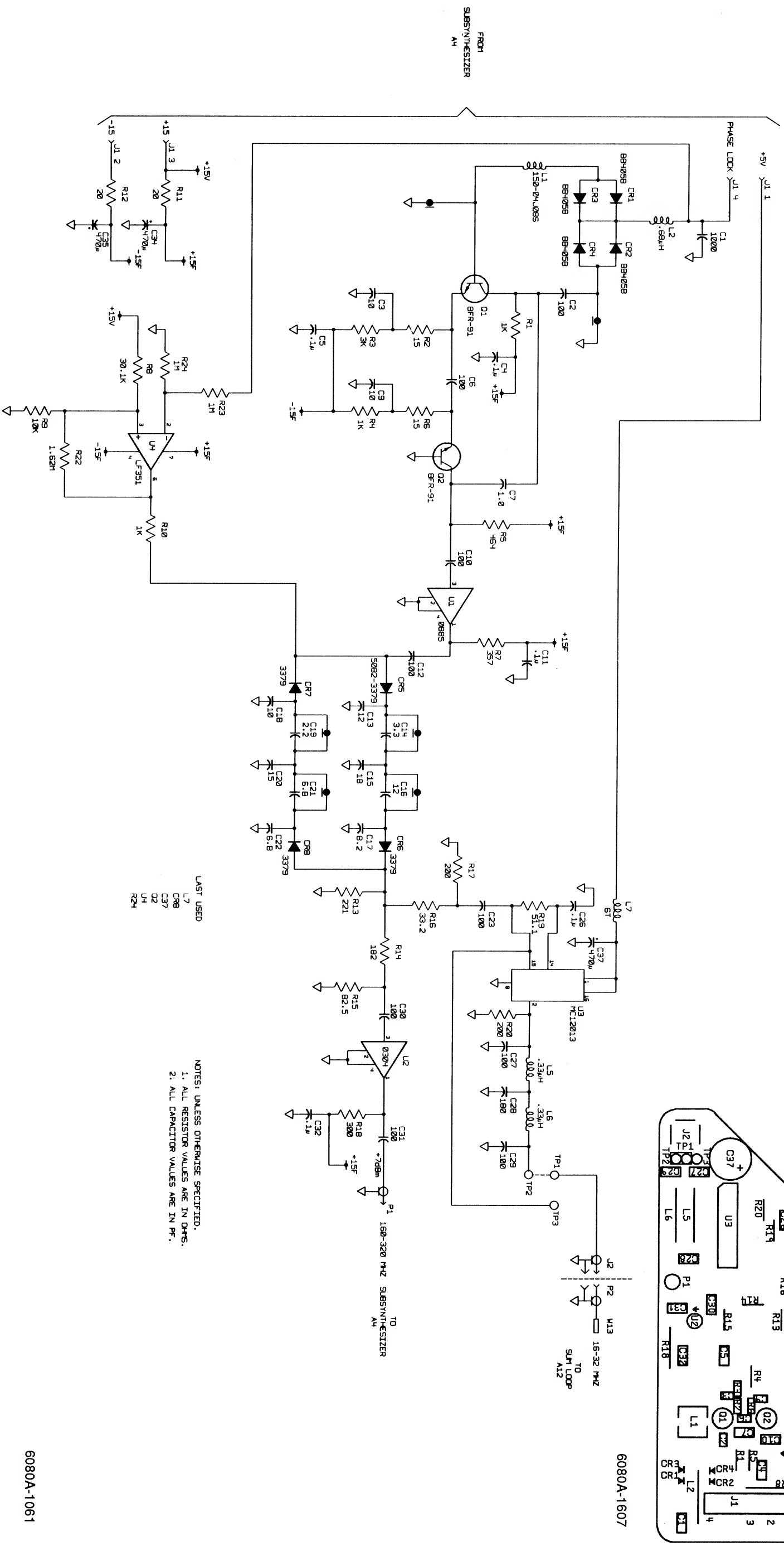
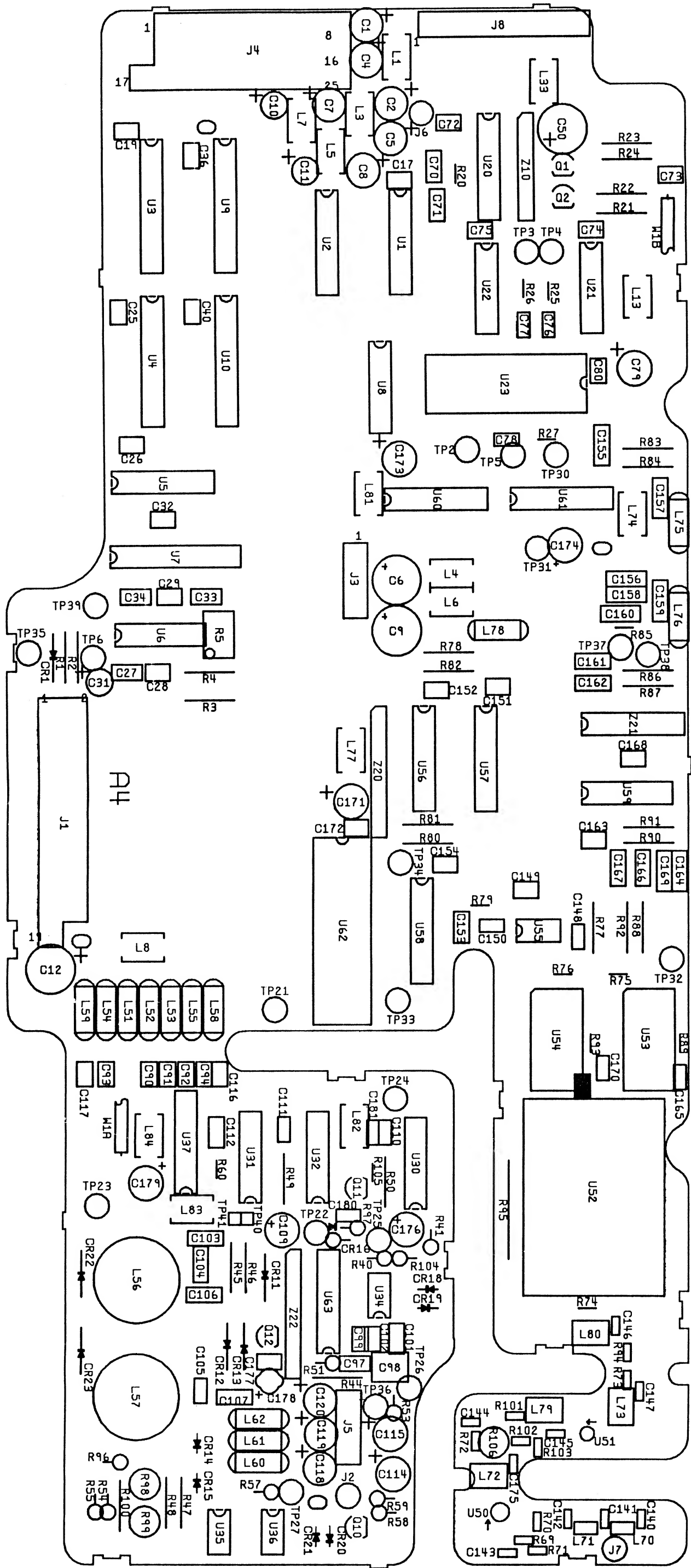


Figure 8-3. A3 Sub-Synthesizer VCO PCA



6080A-1602

Figure 8-4. A4 Sub-Synthesizer PCA

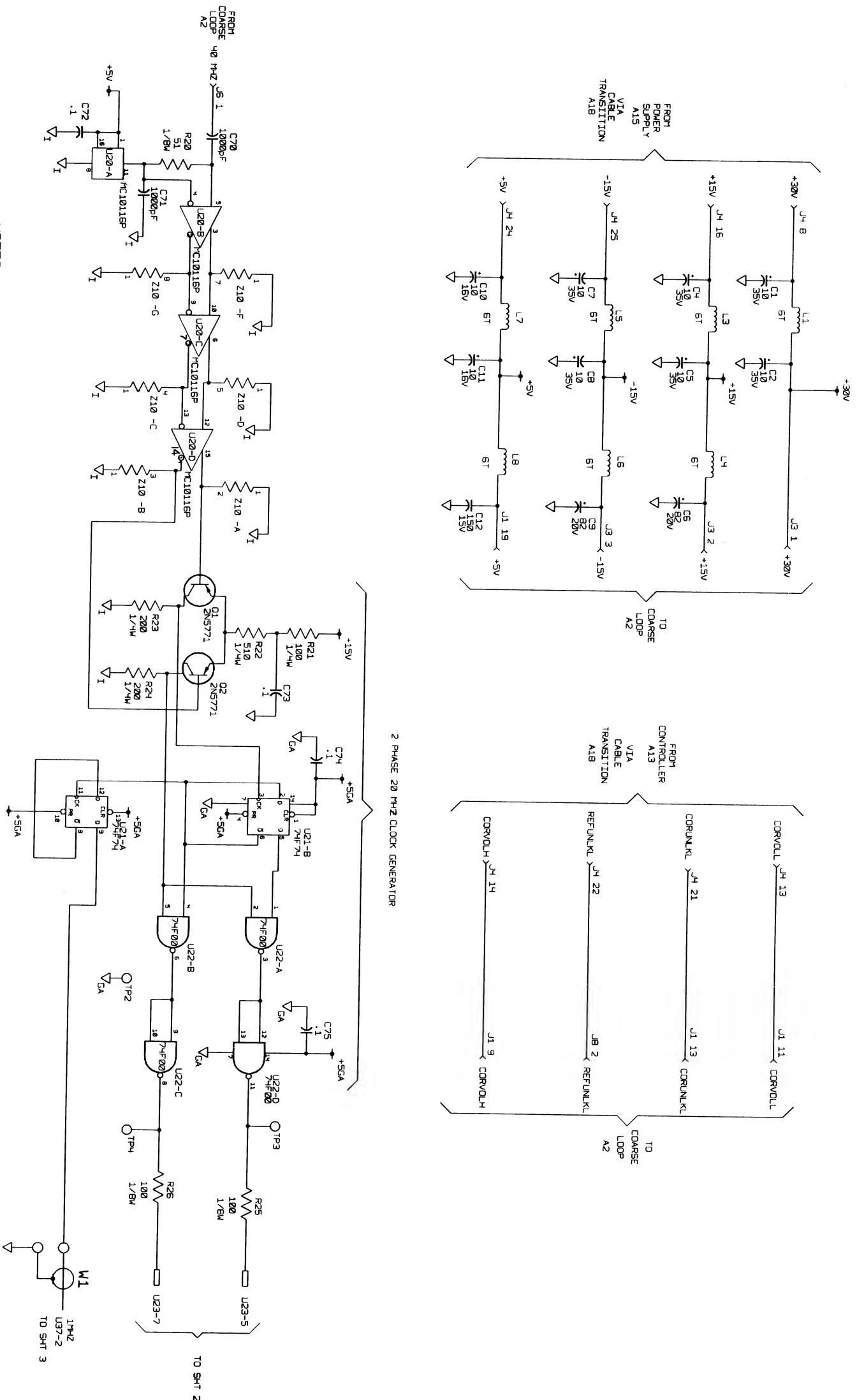


Figure 8-4. A4 Sub-Synthesizer PCA (cont)

SCHEMATIC DIAGRAMS

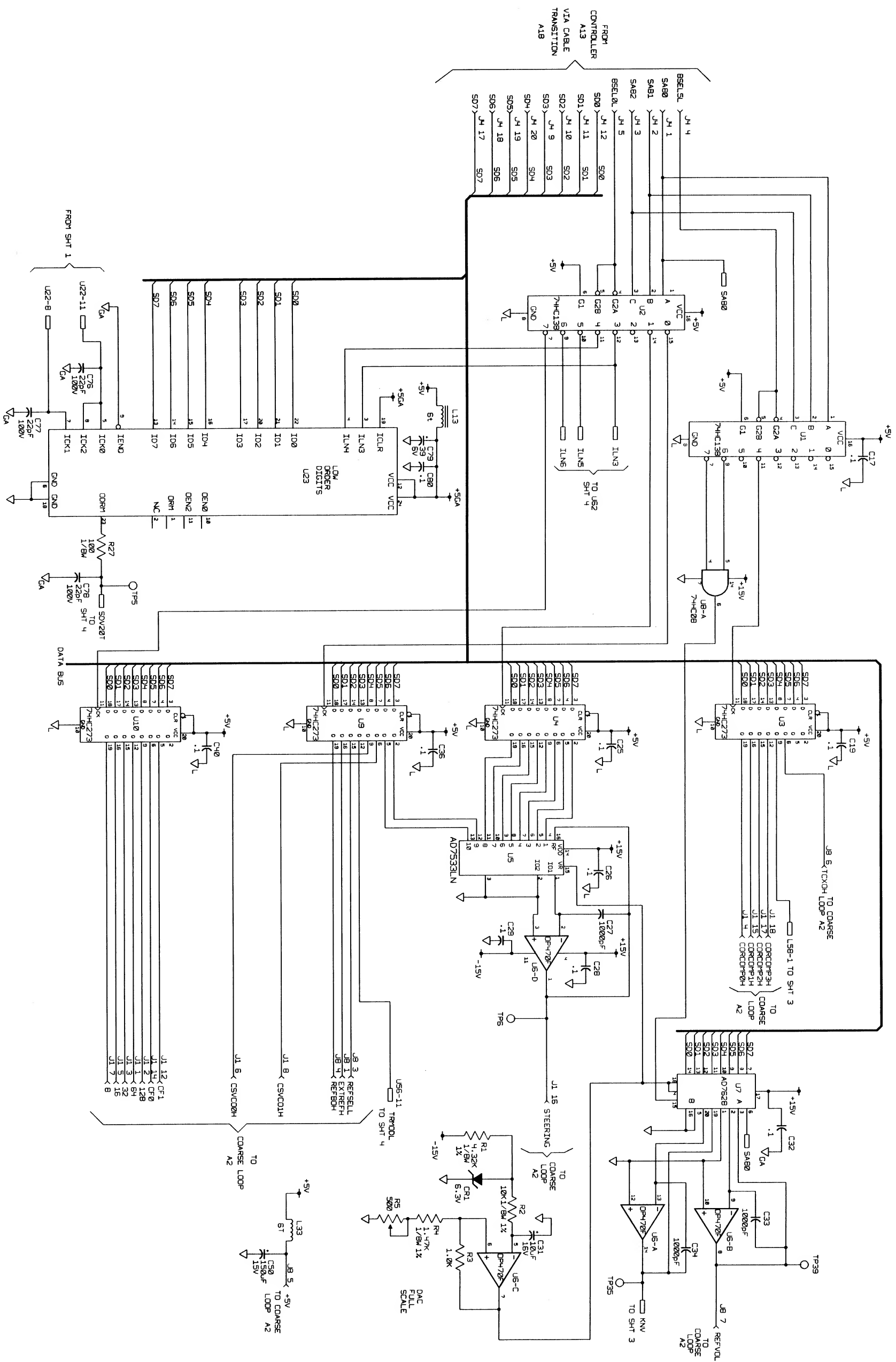


Figure 8-4. A4 Sub-Synthesizer PCA (cont)

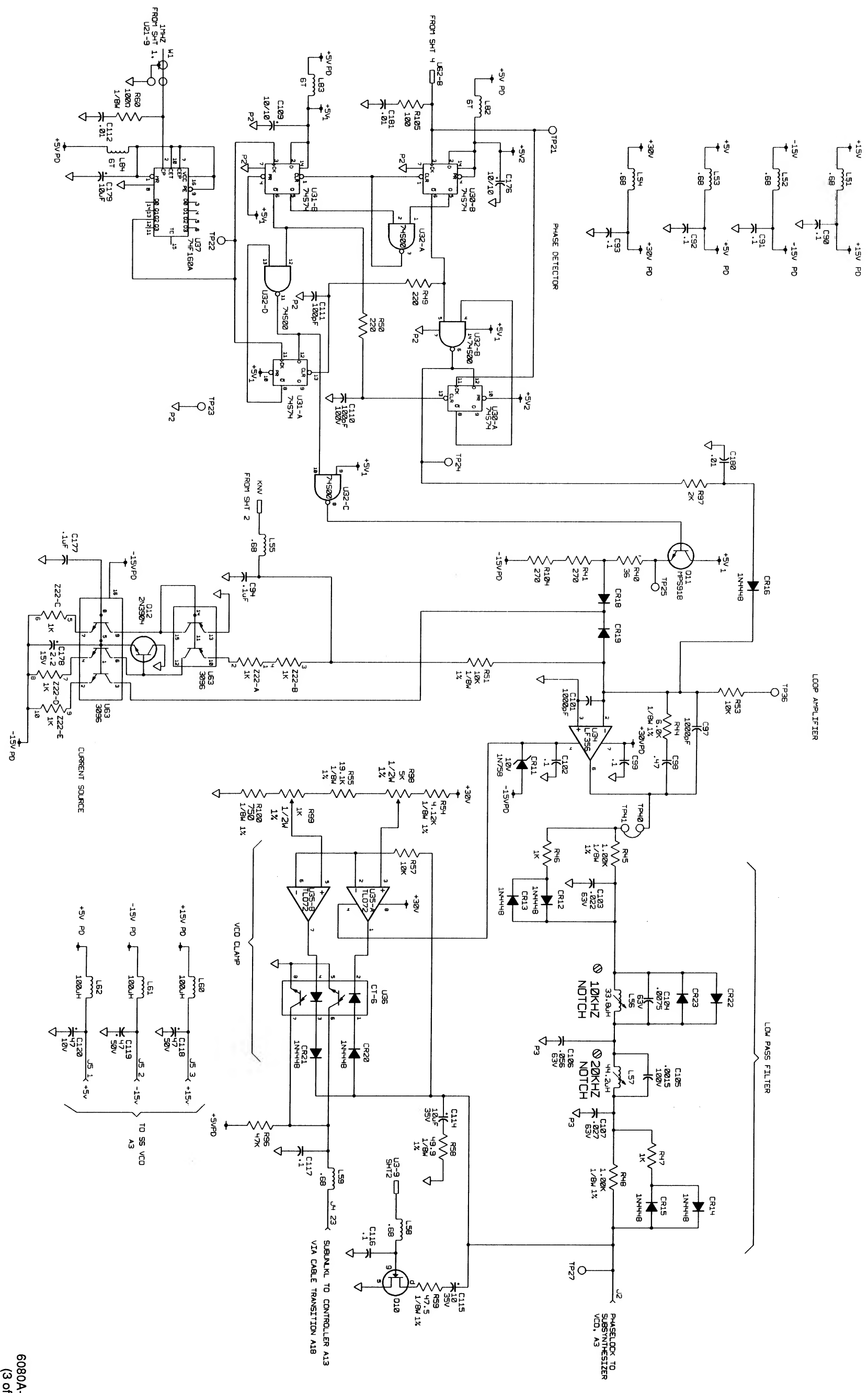


Figure 8-4. A4 Sub-Synthesizer PCA (cont)

SCHEMATIC DIAGRAMS

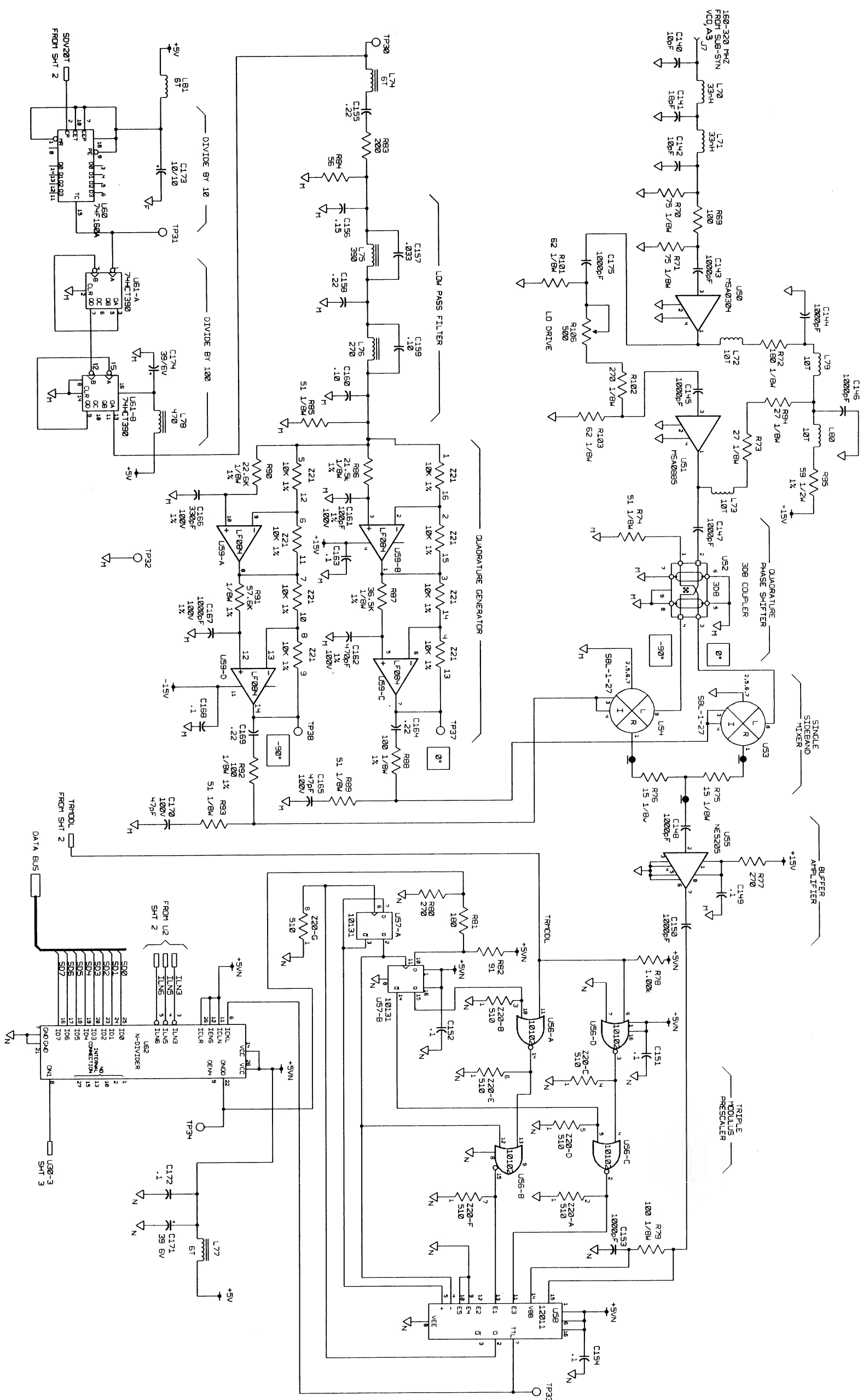


Figure 8-4. A4 Sub-Synthesizer PCA (cont)

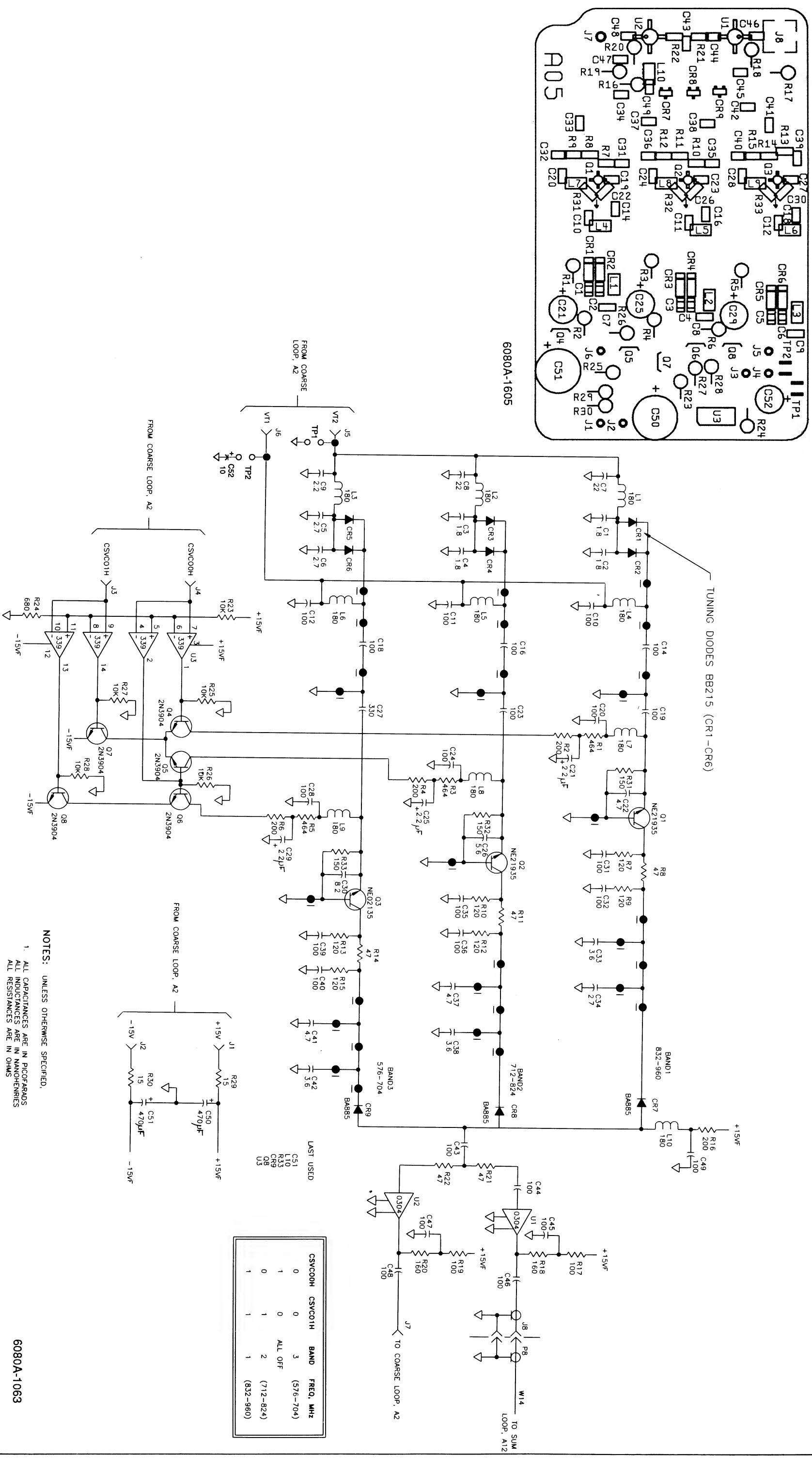
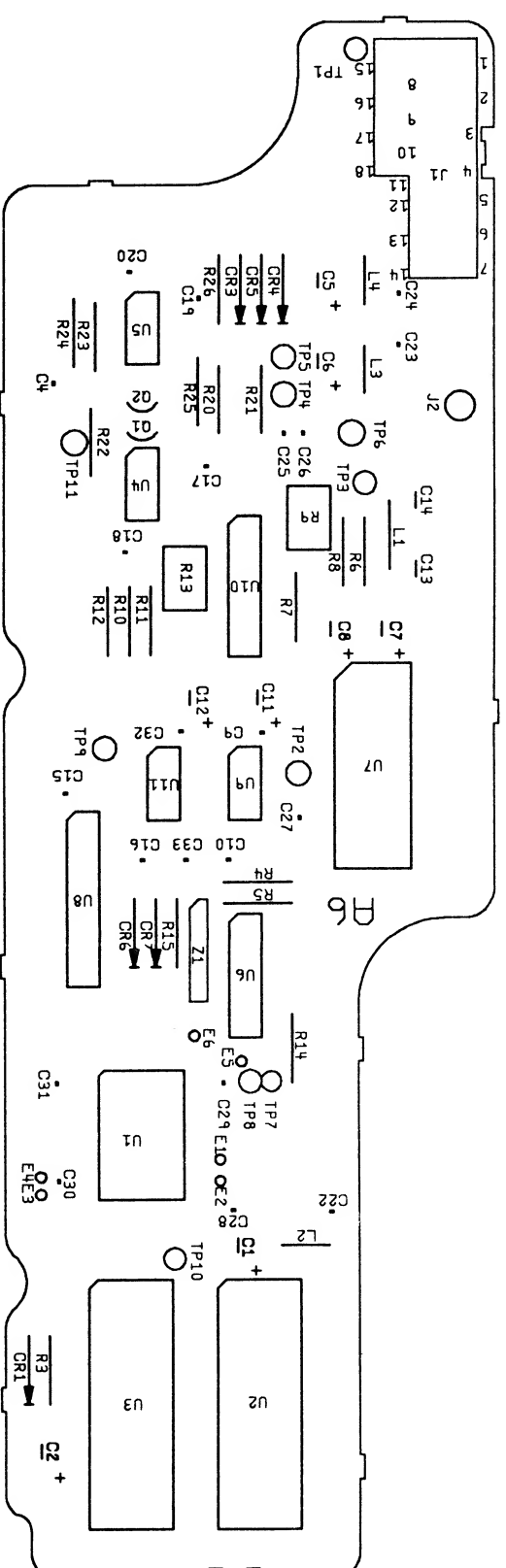


Figure 8-5. A5 Coarse Loop VCO PCA

SCHEMATIC DIAGRAMS



6080A-1602

REF.	V _{CC}	+15V	-15V	▽	▽	#PINS	TYPE
U1	2.23 42.63	37		3.22, 33, 43, 62		80	65022 ARRAY
U2	1.28			14		28	27256
U3	1.28			14		28	27256
U4		7	4			8	LM6361
U5		7	4			8	LM6361
U6		3	12			14	LM339
U7		3	7	12	5	24	AD565A
U8		18		3	2	20	AD7548
U9		8	4			8	LF412A
U10		13	4		5	16	DG308
U11		8	4			8	LF412A

NOTE: (unless otherwise specified)

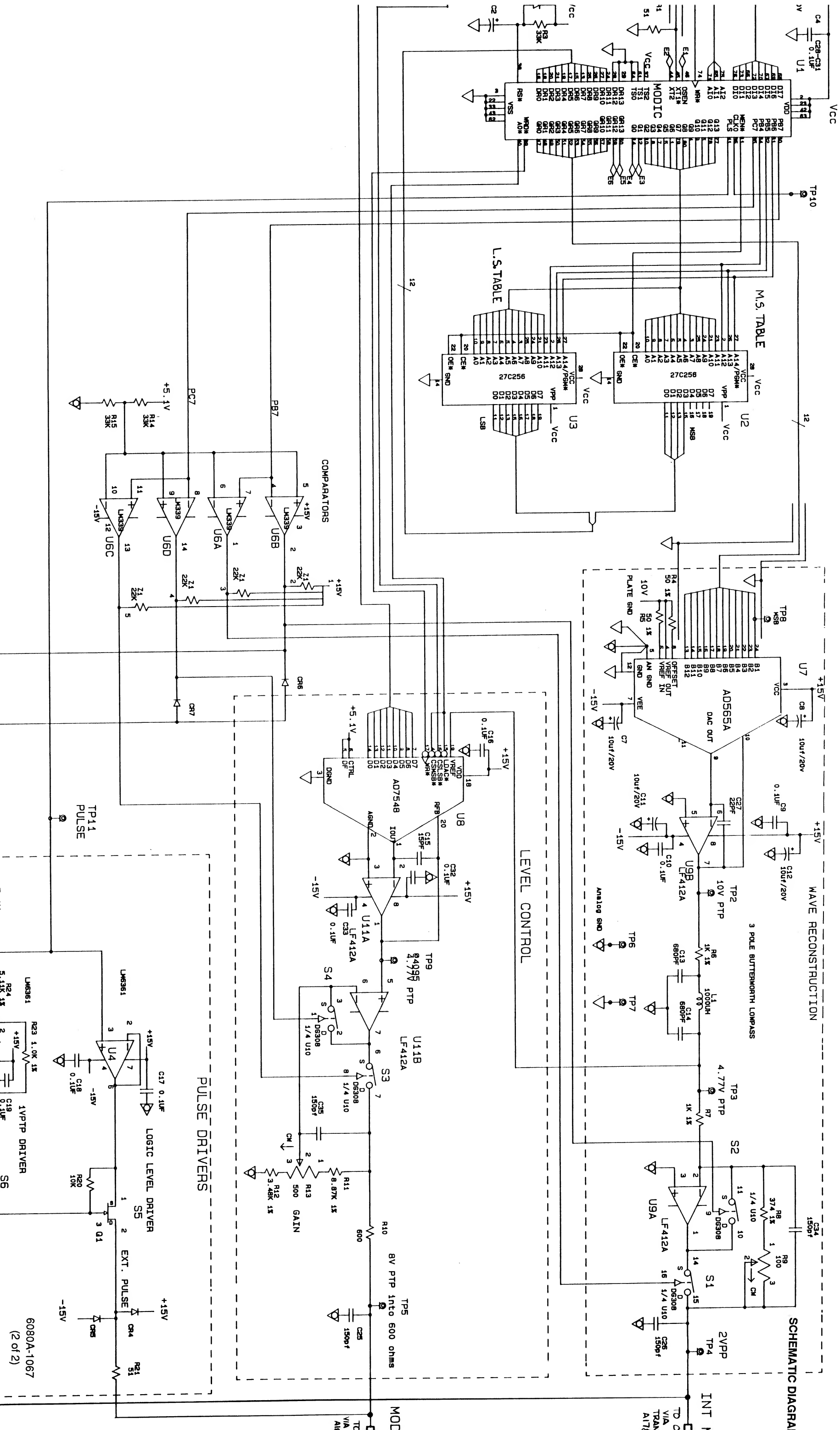
- 1) All capacitor values are in μF .
- 2) All resistor values are in Ohms.

Waveform selection table:

PB<6>		PB<5>		PB<4>		Waveform selected:
-------	--	-------	--	-------	--	--------------------

PB <7>	PC <7>	MODE <1>	MODE <0>	S1 S6	S2 S6	S3 S6	S4 S6	S5 S6	Operation:
0	0	0	0	0	0	0	0	0	pulse generation
1	1	0	0	0	0	0	0	0	MOD OUT and INT
1	1	1	0	0	0	0	0	0	MOD OUT and INT
1	1	1	1	0	0	0	0	0	MOD OUT and INT
0	1	1	0	0	0	0	0	0	MOD OUT is on
1	0	1	0	0	0	0	0	0	INT MOD is on
1	0	0	0	0	0	0	0	0	INT MOD is on
1	0	0	1	0	0	0	0	0	MOD OUT is on

Sn- refers to switches on the PWB.

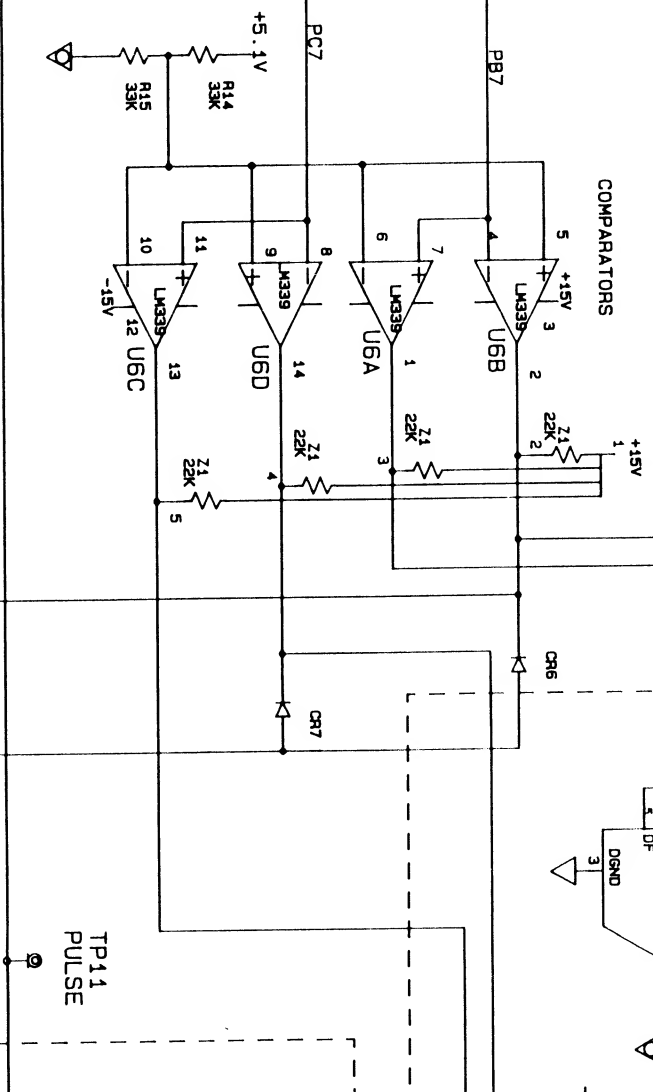
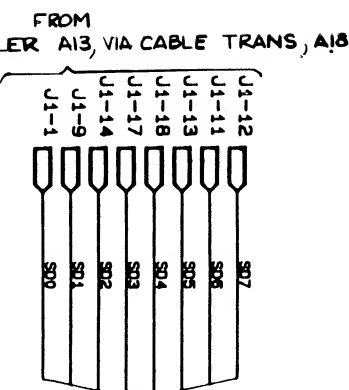
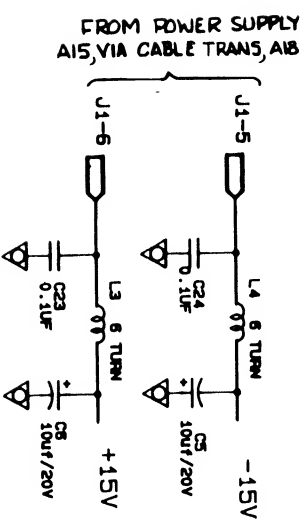
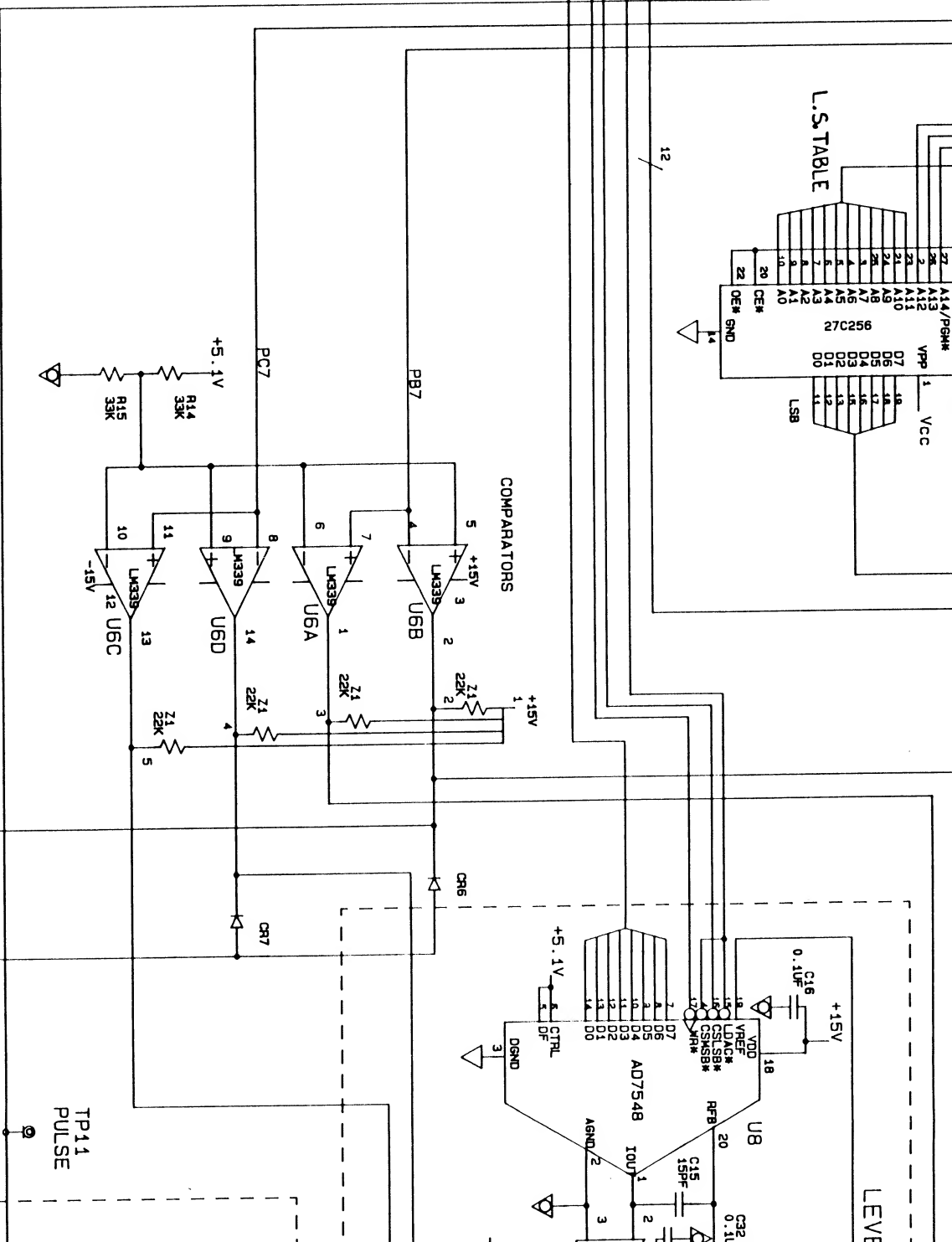
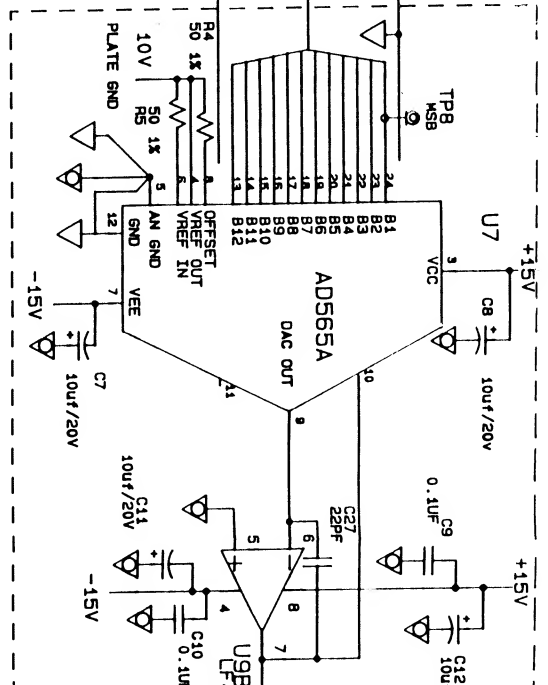
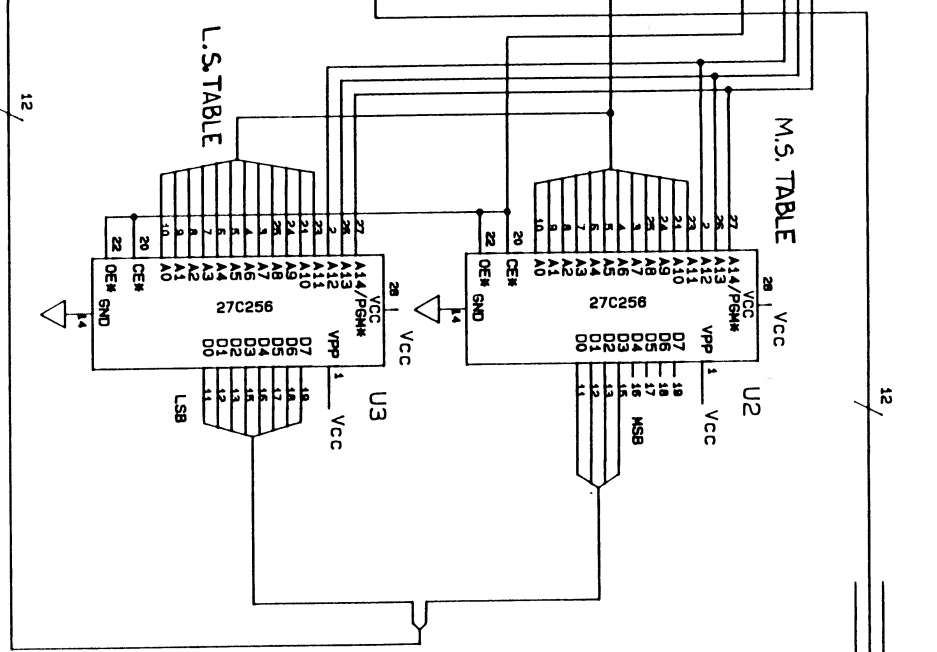
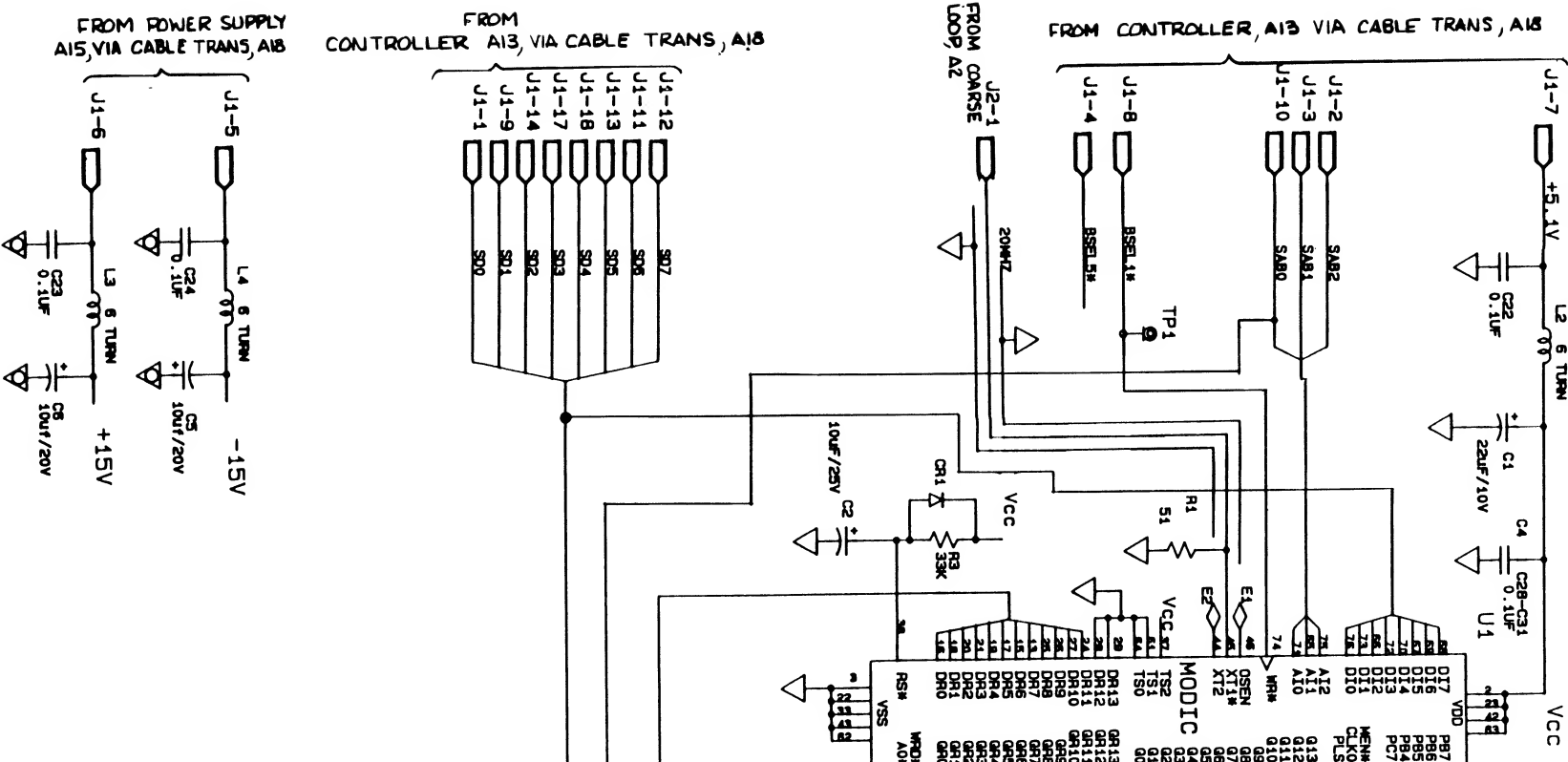


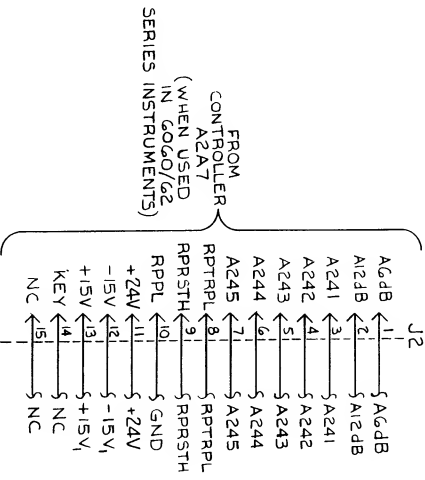
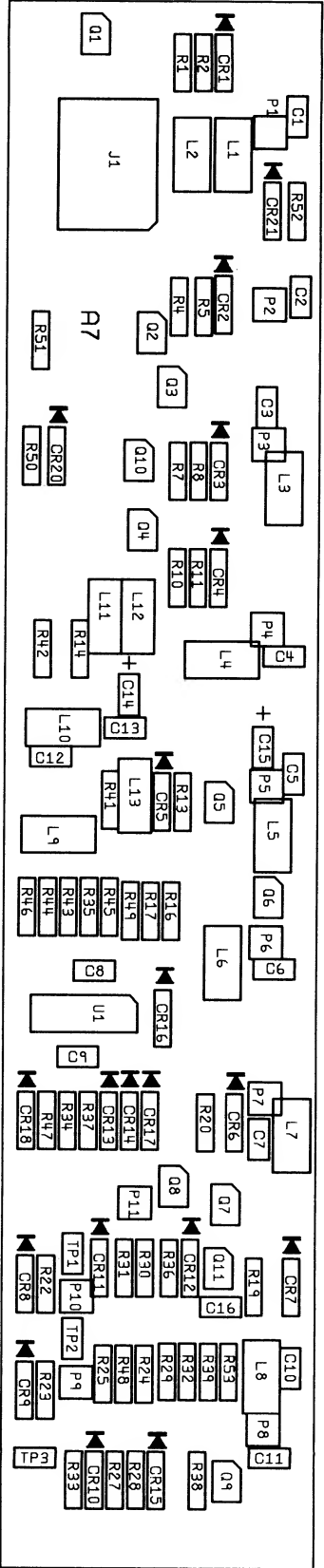
SCHEMATIC DIAGRAM

WAVE RECONSTRUCTION

LEVEL CONTROL

PULSE DRIVERS





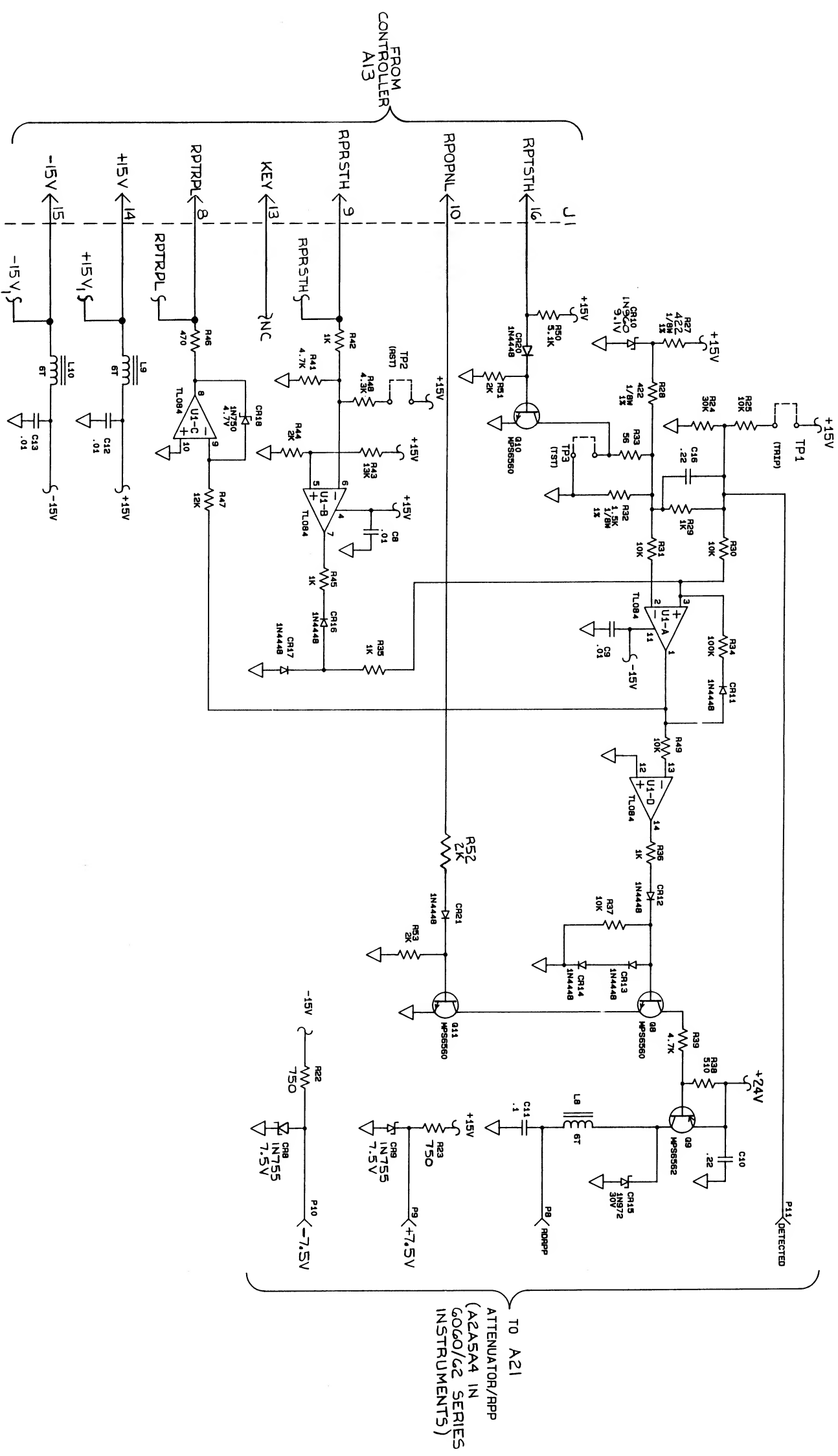
TO A21
ATTENUATOR/
RPP

- NOTES: (UNLESS OTHERWISE SPECIFIED)
1. ALL RESISTORS ARE 1/4W, 5%.
 2. ALL CAPACITOR VALUES ARE IN MICROFARADS.

REFERENCE	DESIGNATIONS
LAST USED	NOT USED
U1	
Q11	
L13	
C16	
CR21	
R53	
TP3	
J1	
P11	
	R28

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(1 of 2)

Figure 8-7. A7 Relay Driver PCA



6080A-1032
(2 of 2)

Figure 8-7. A7 Relay Driver PCA (cont)

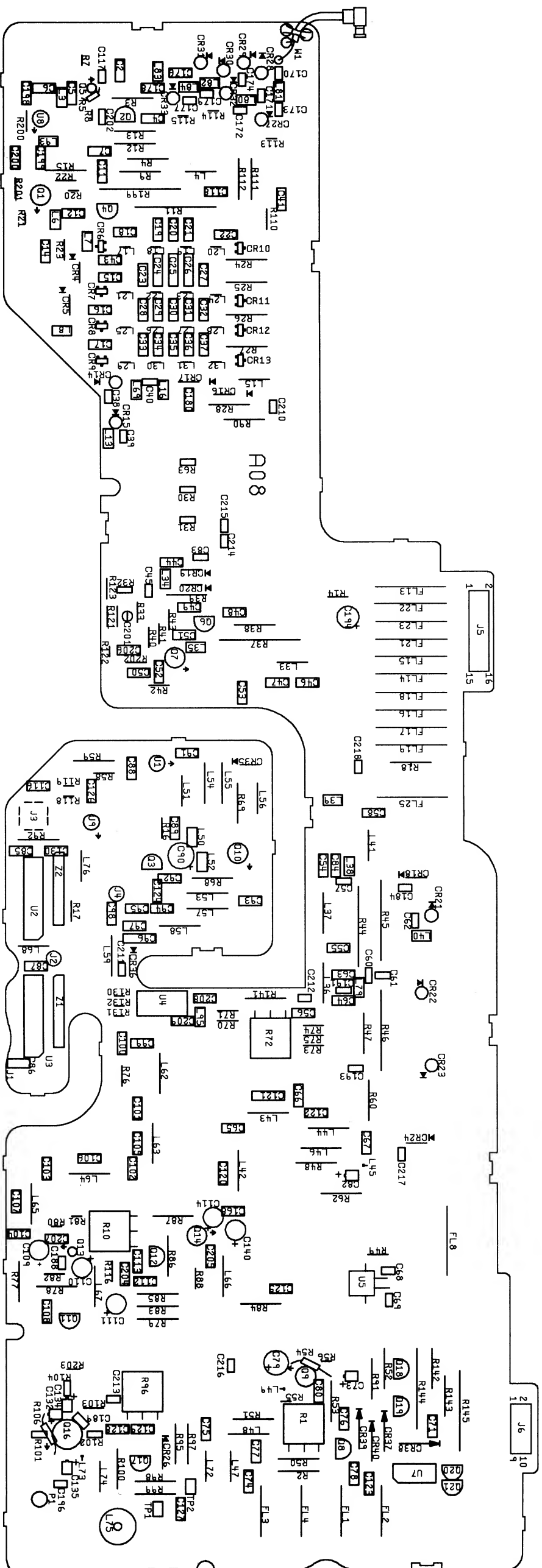


Figure 8-8. A8 Output PCA

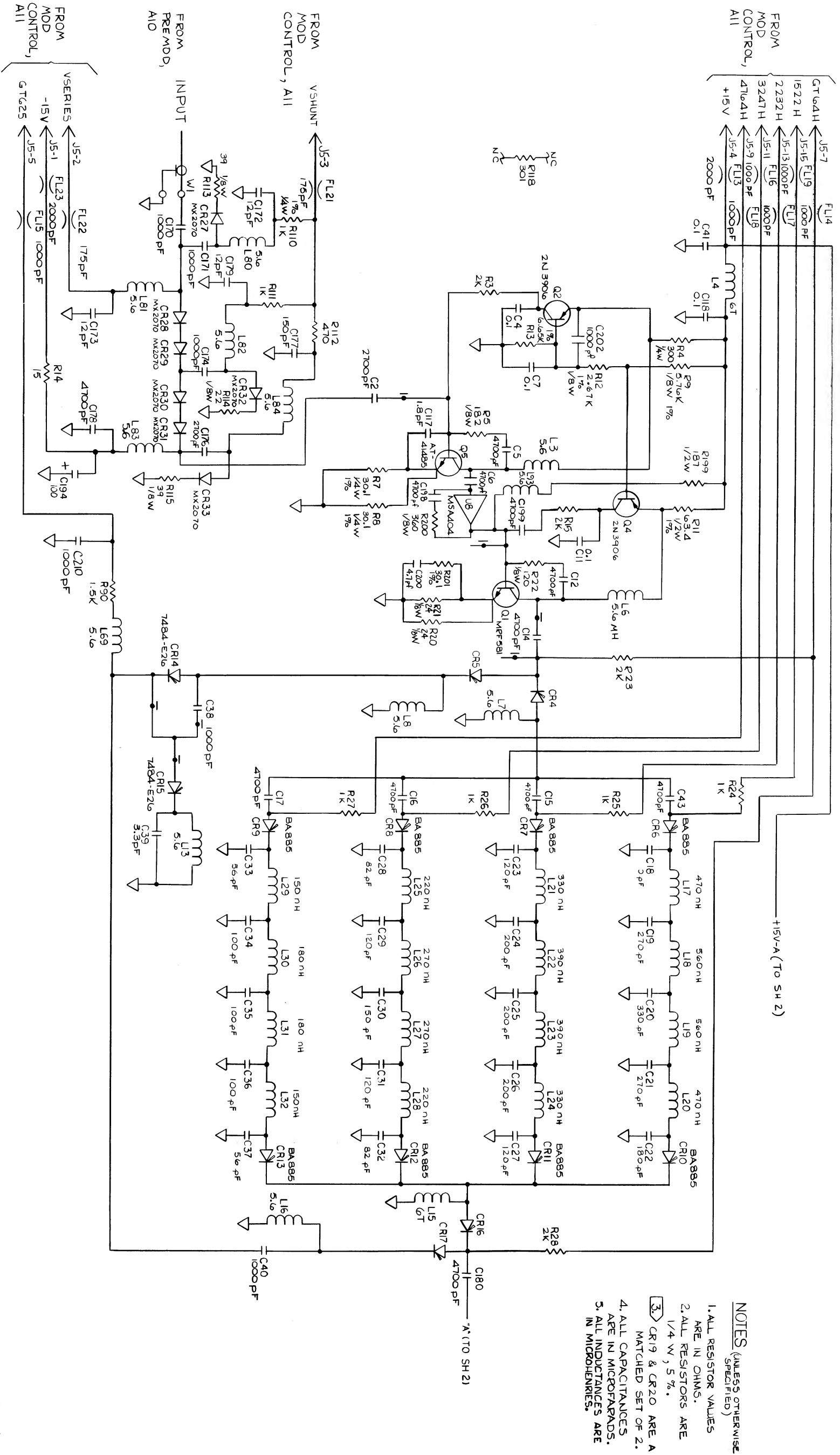


Figure 8-8. A8 Output PCA (cont)

6080A-1040
(1 of 3)

SCHEMATIC DIAGRAMS

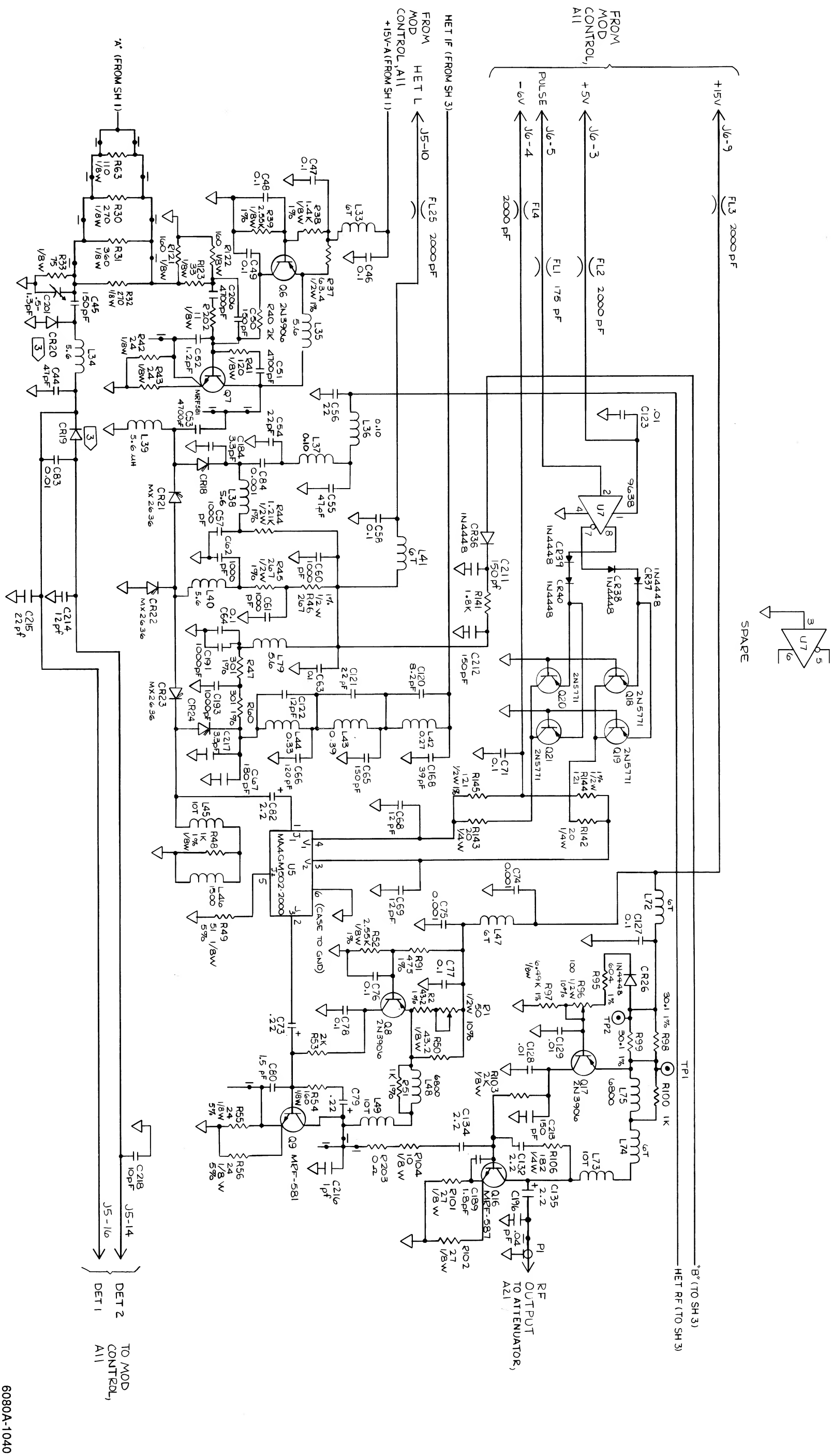


Figure 8-8. A8 Output PCA (cont)

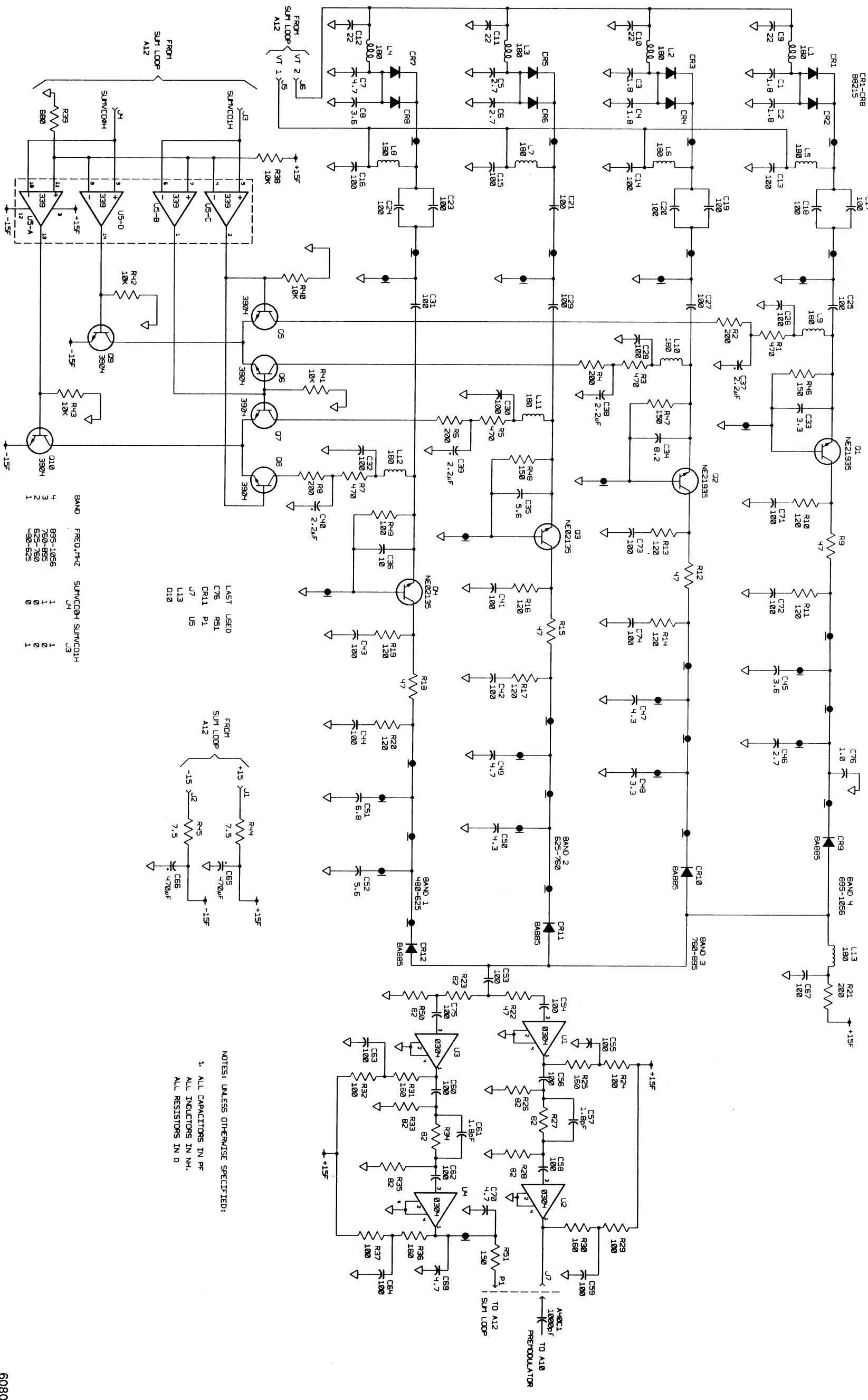


Figure 8-9. A9 Sum Loop VCO PCA (cont)

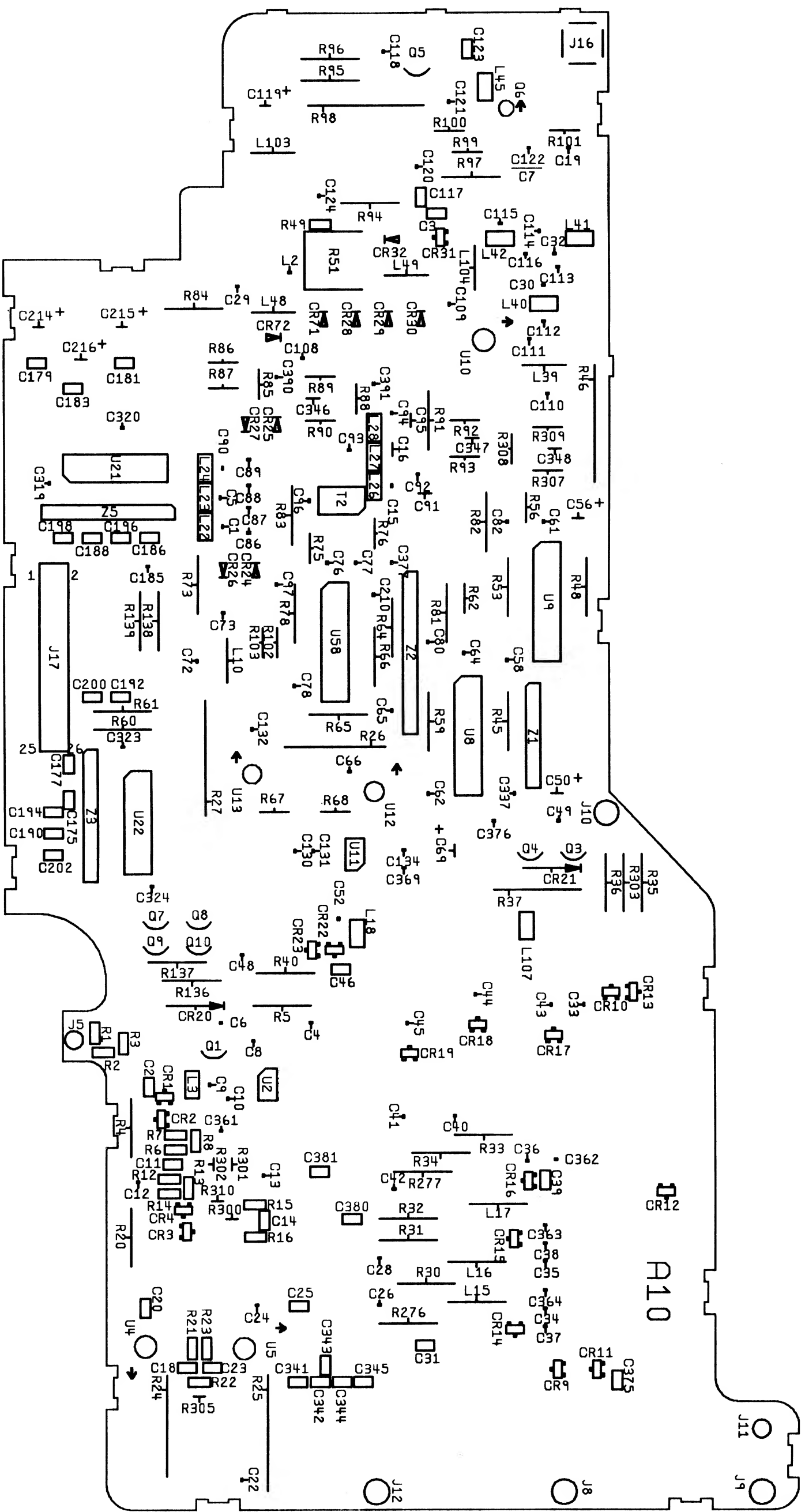
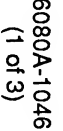


Figure 8-10. A10 Premodulator PCA

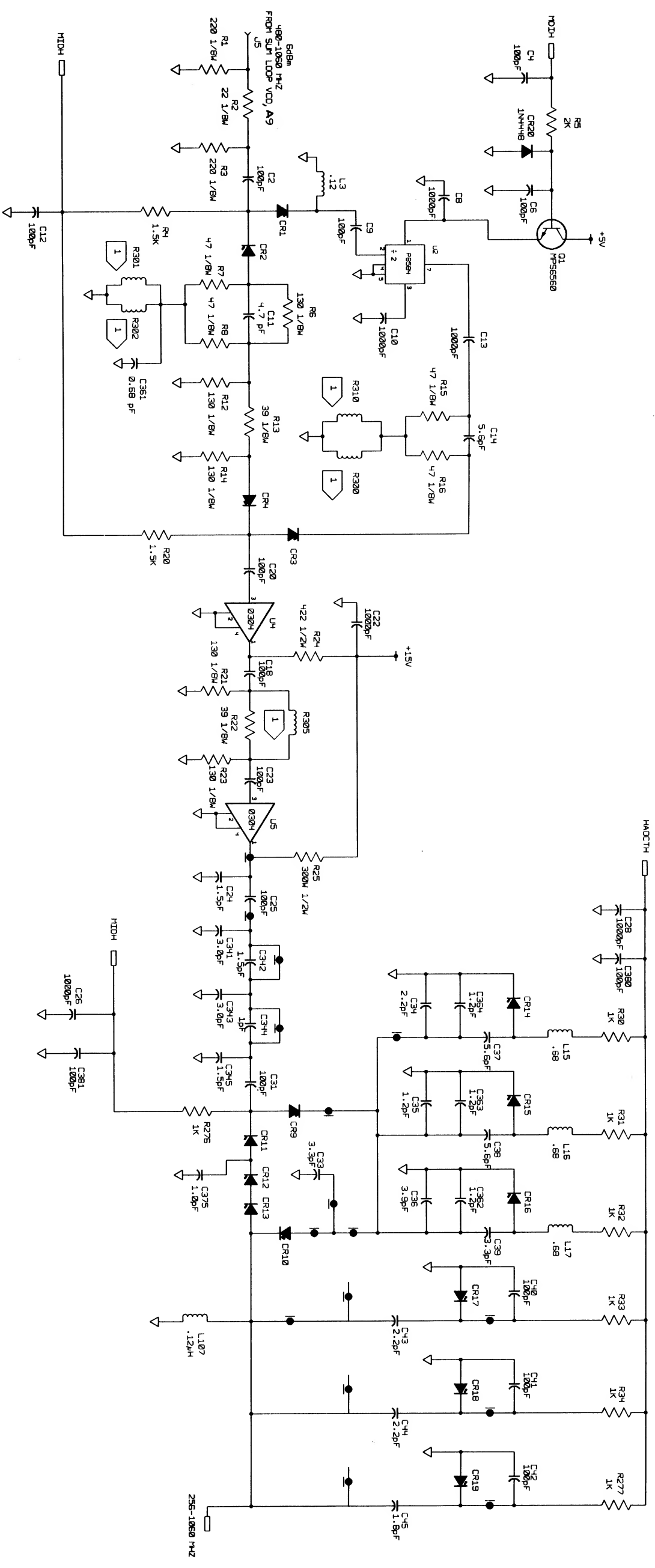


NOTES: (UNLESS OTHERWISE SPECIFIED.

1. RESISTORS INDICATED FUNCTION AS INDUCTORS IN THIS CIRCUIT.
($< 0.02 \text{ OHMS}$, $0.000M$)
2. ALL RESISTOR VALUES ARE IN OHMS.
ALL CAPACITOR VALUES ARE IN μF 'S.
ALL INDUCTOR VALUES ARE IN μH 'S.

Figure 8-10. A10 Premodulator PCA (cont)

SCHEMATIC DIAGRAMS



FREQ	.01 - 258 -	256 + - 350 -	350 + - 512 -	512 + - 730 -	730 + - 1056
MIDTH	+12V	+12V	+12V	-13V	-13V
HADCTH	-13V	+12V	-13V	-13V	+12V

Figure 8-10. A10 Premodulator PCA (cont)

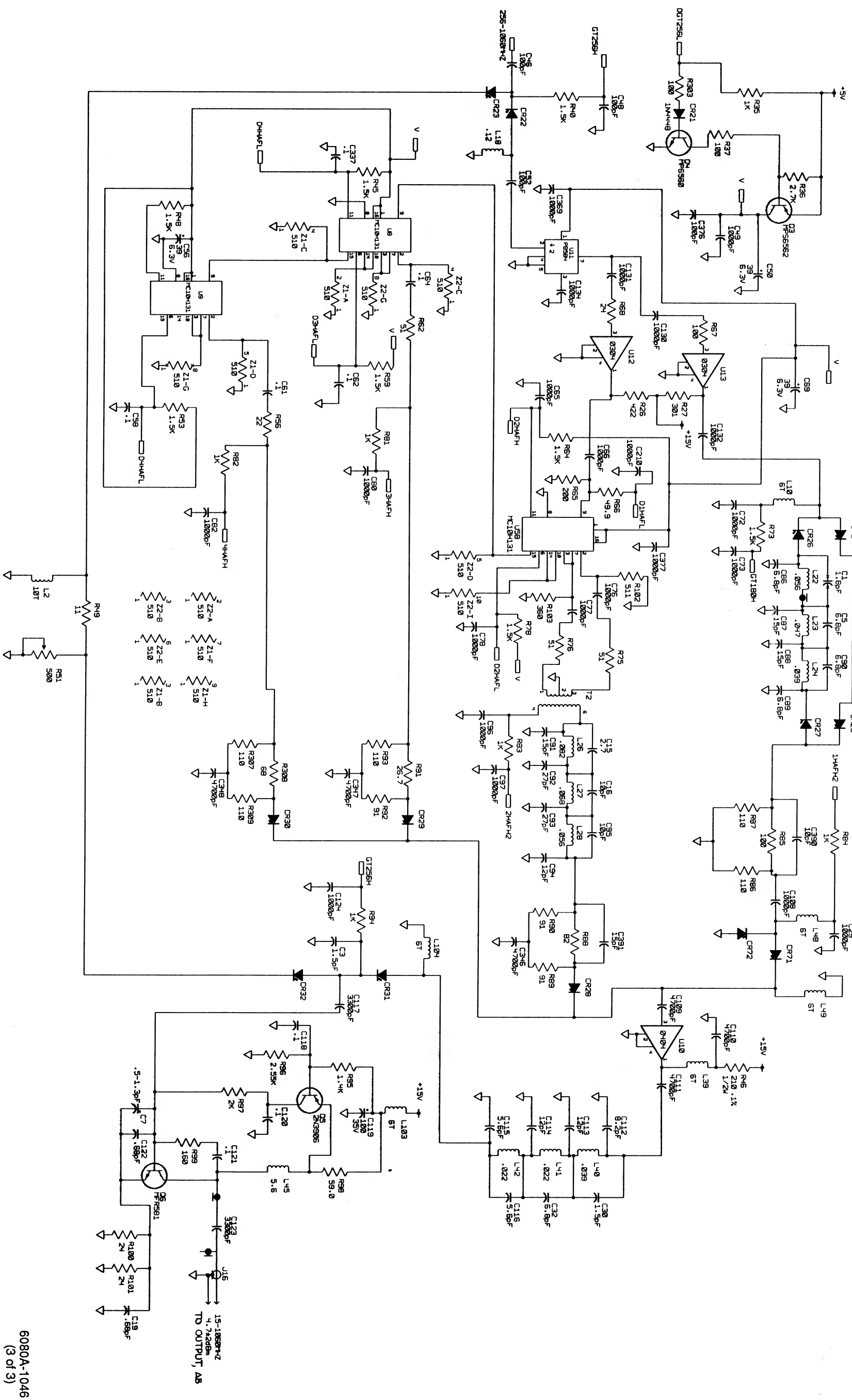
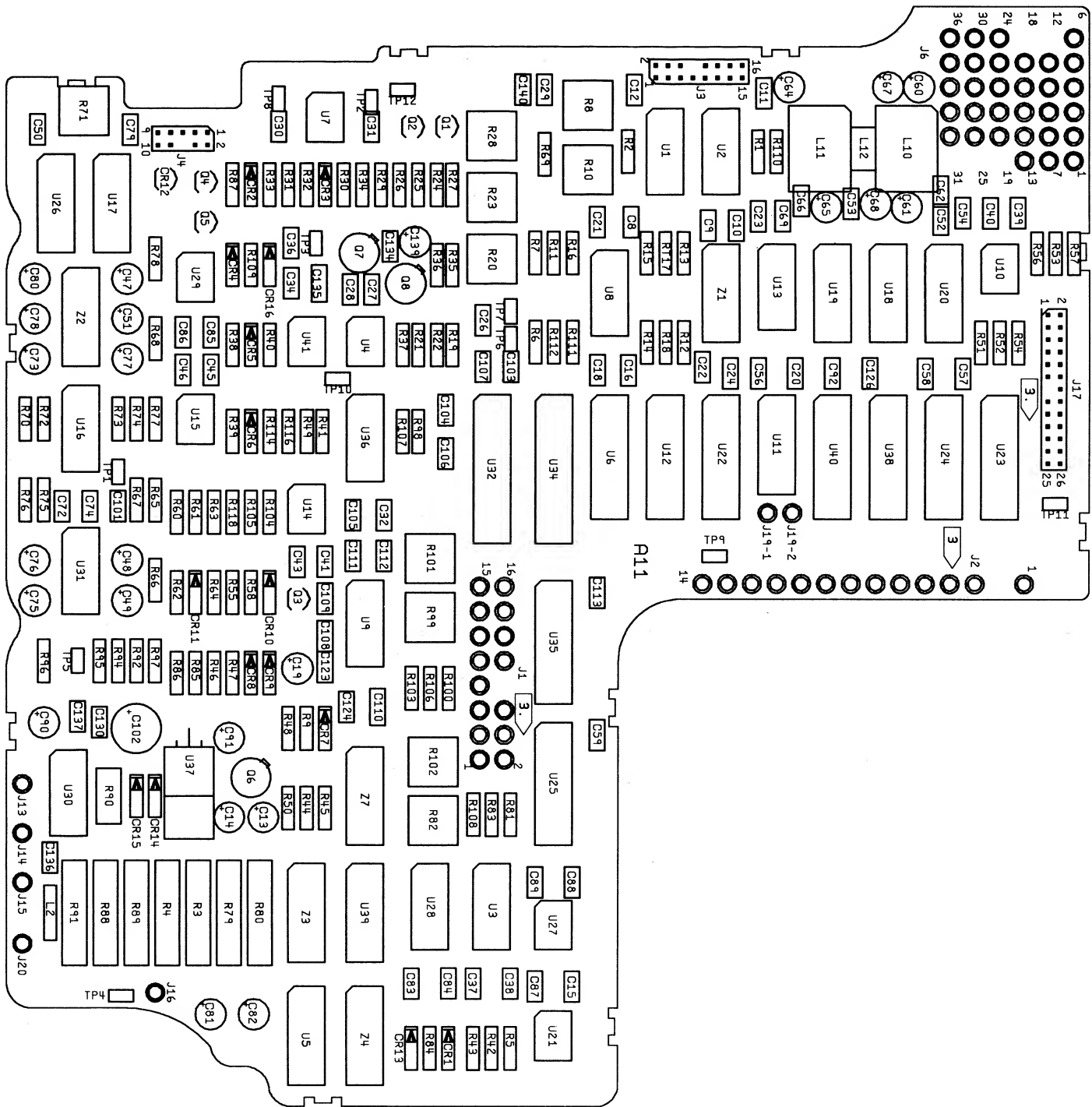


Figure 8-10. A10 Premodulator PCA (cont)

6080A-1046
(3 of 3)



6080A-1601

Figure 8-11. A11 Modulation Control PCA

SCHEMATIC DIAGRAMS

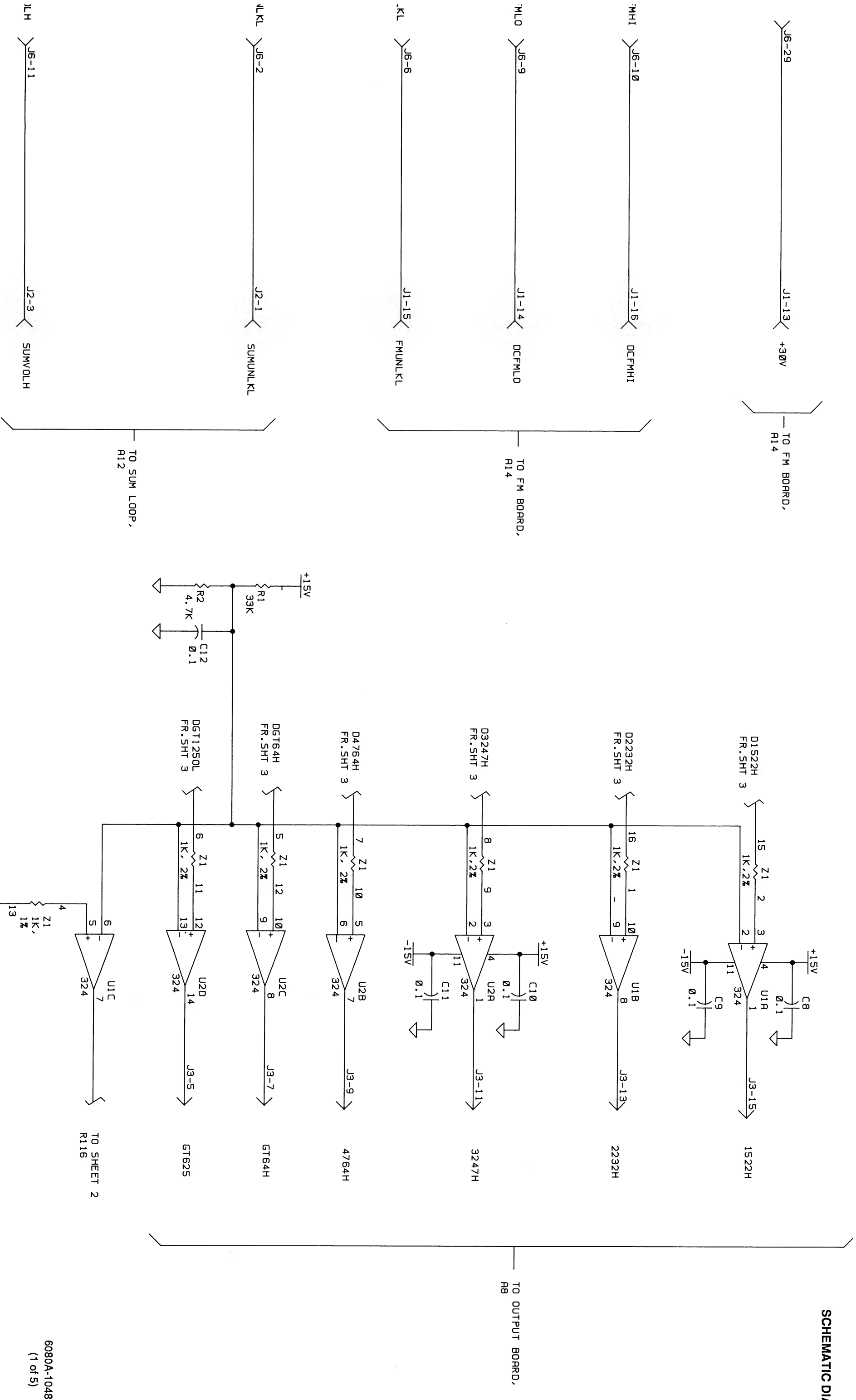
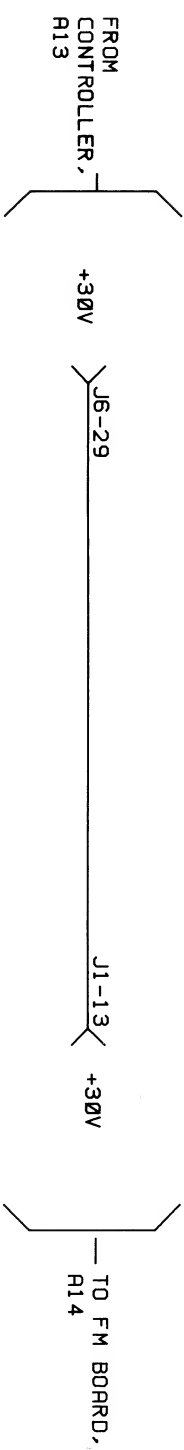
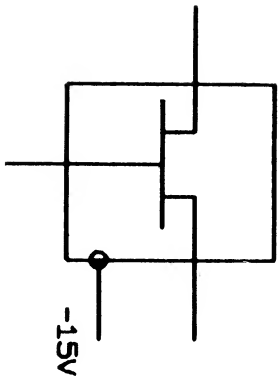


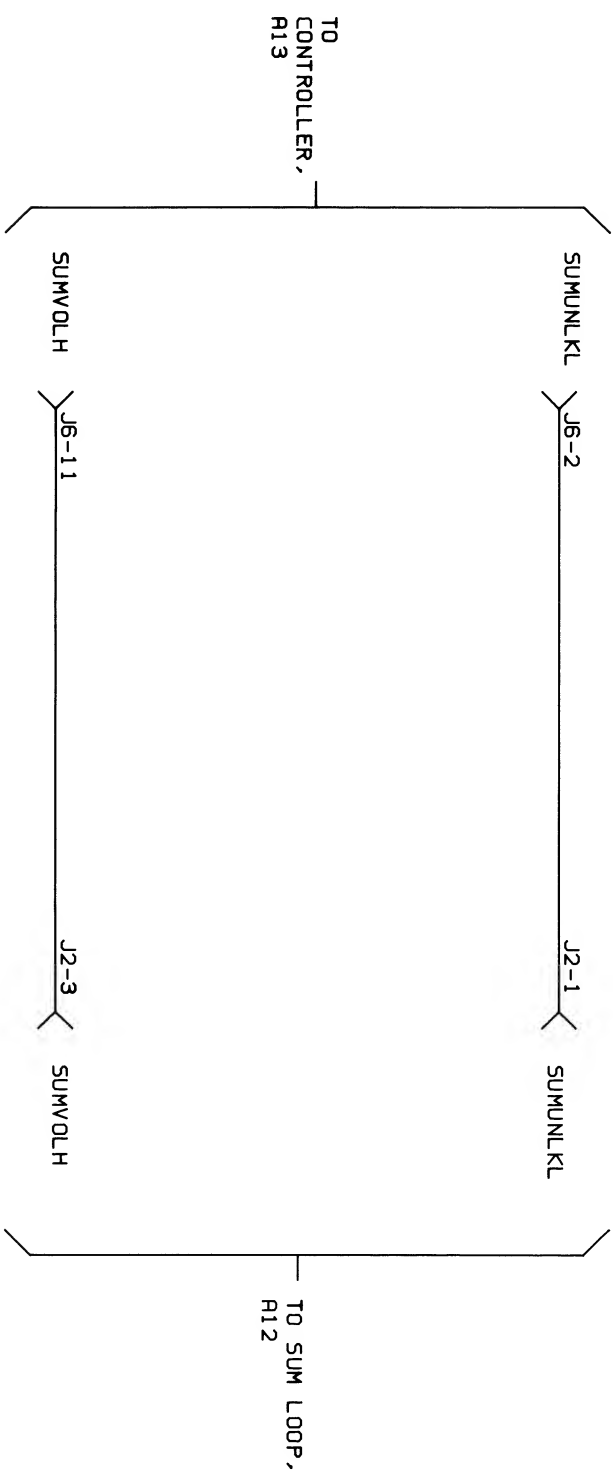
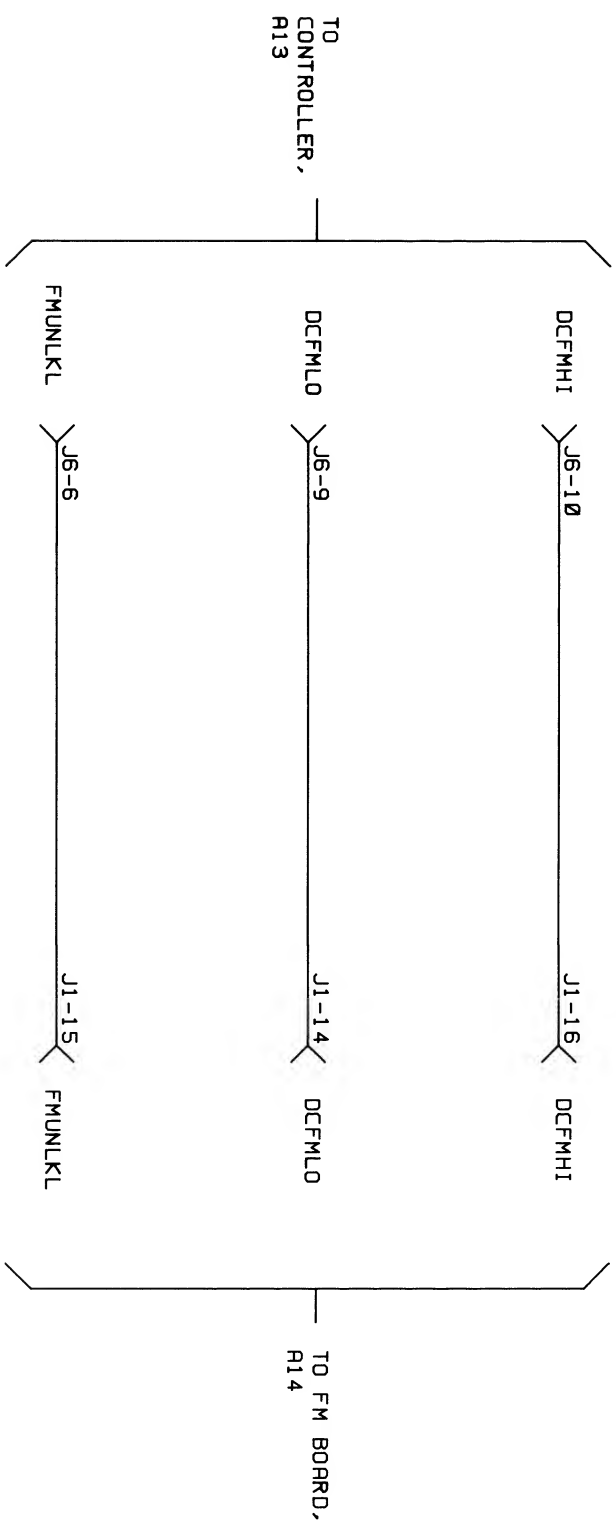
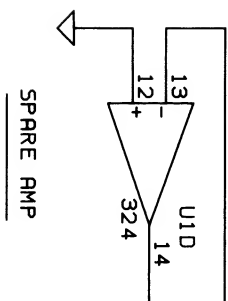
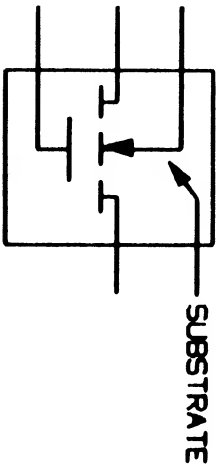
Figure 8-11. A11 Modulation Control PCA (cont)



NOTE:
SD5002'S WHICH
ARE SHOWN AS:

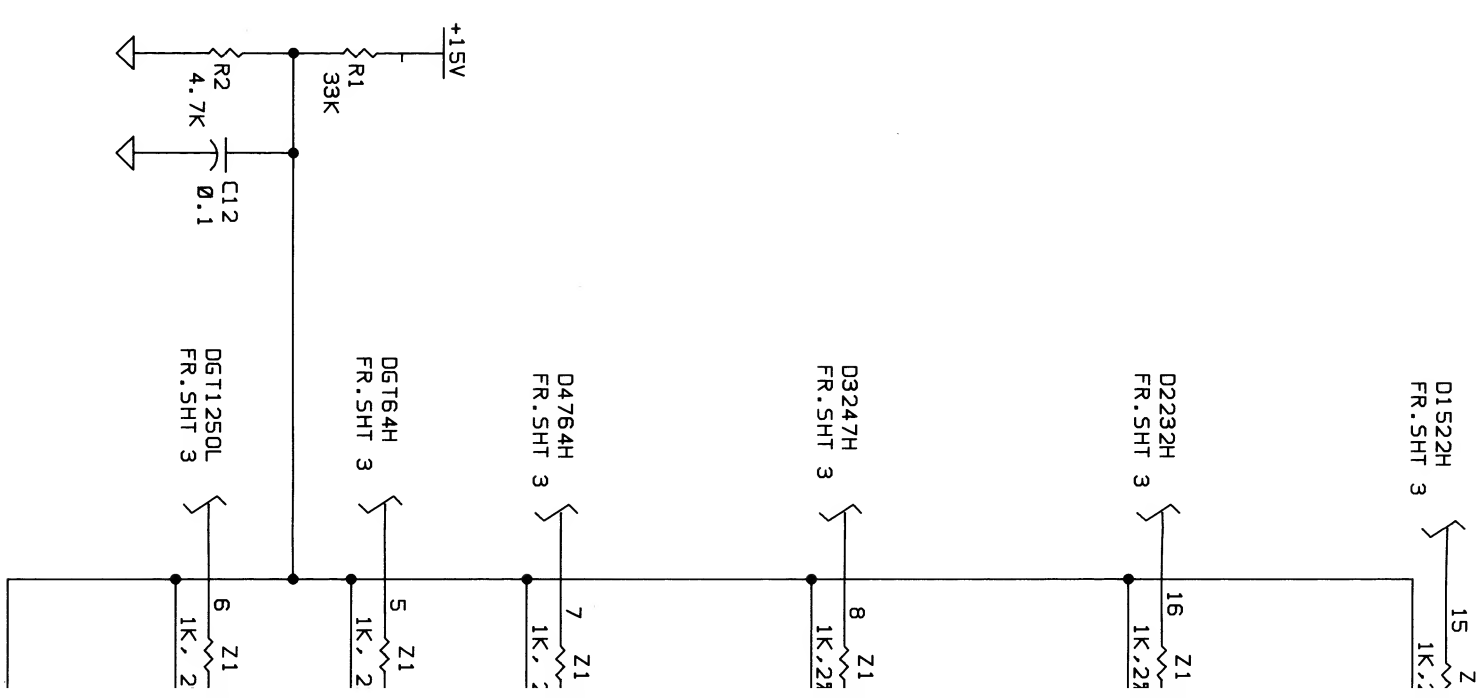


ARE ACTUALLY
CONSTRUCTED:



LAST USED:
C140, CR16, L12, Q8,
R118, TP12, U41, Z7

NOT USED:
C1-C7, C17, C25, C33,
C35, C42, C44, C55, C63,
C70-C71, C93-C100, C114,
C122, C125, C127-C129,
C131-C133, C138, L1, L3-L9,
R59, R113, R115, R117,
U33, Z5-Z6



SCHEMATIC DIAGRAMS

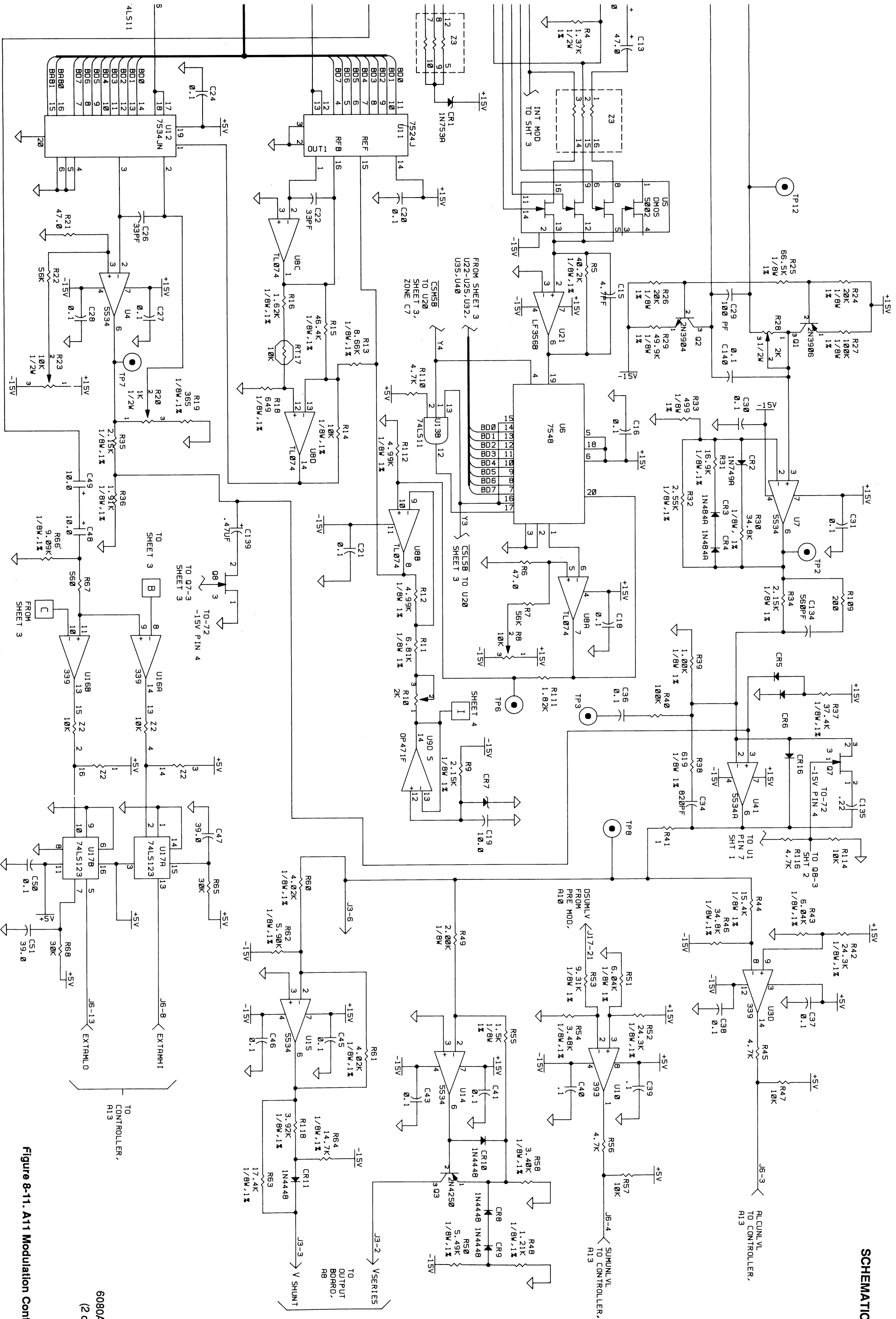
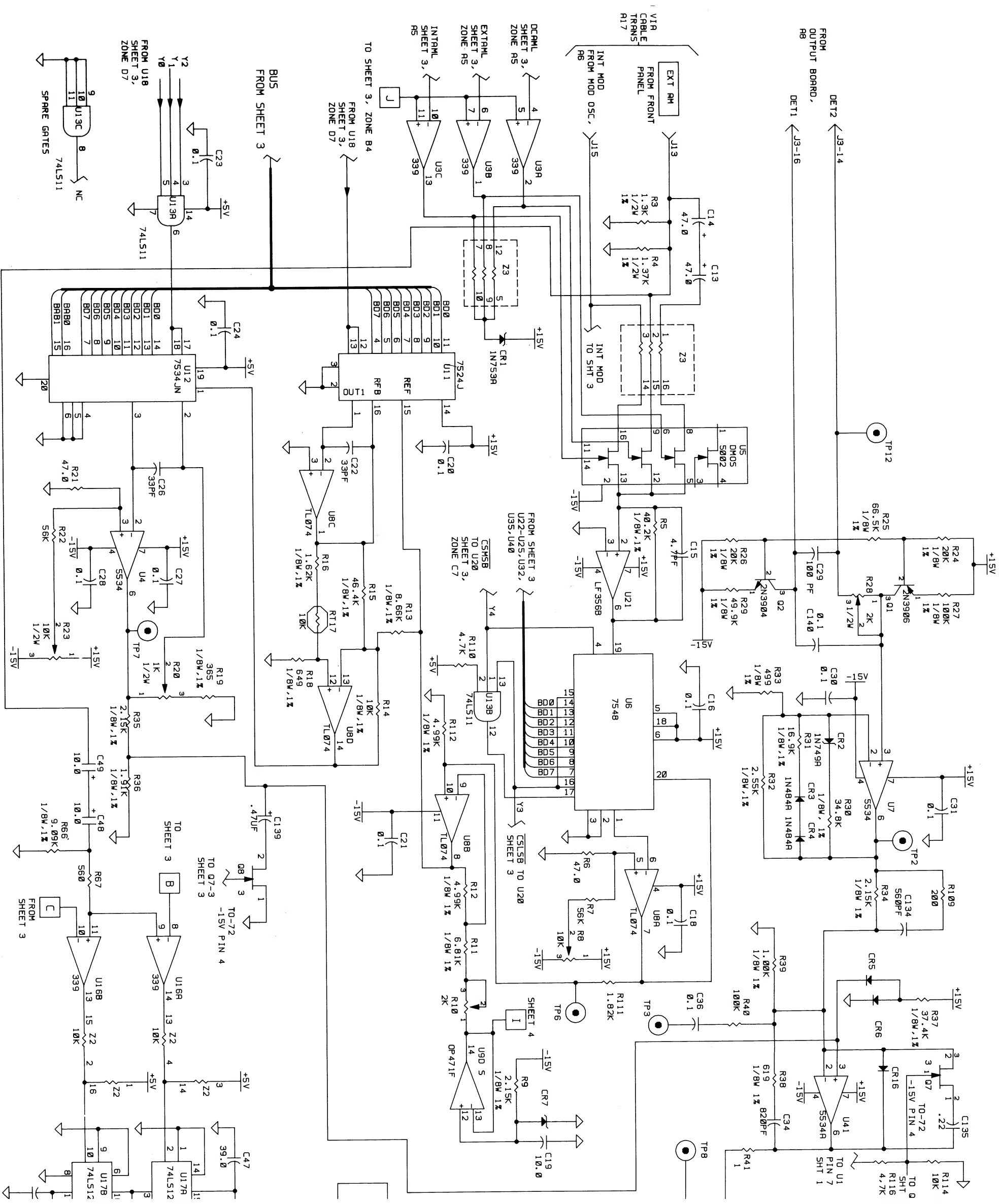
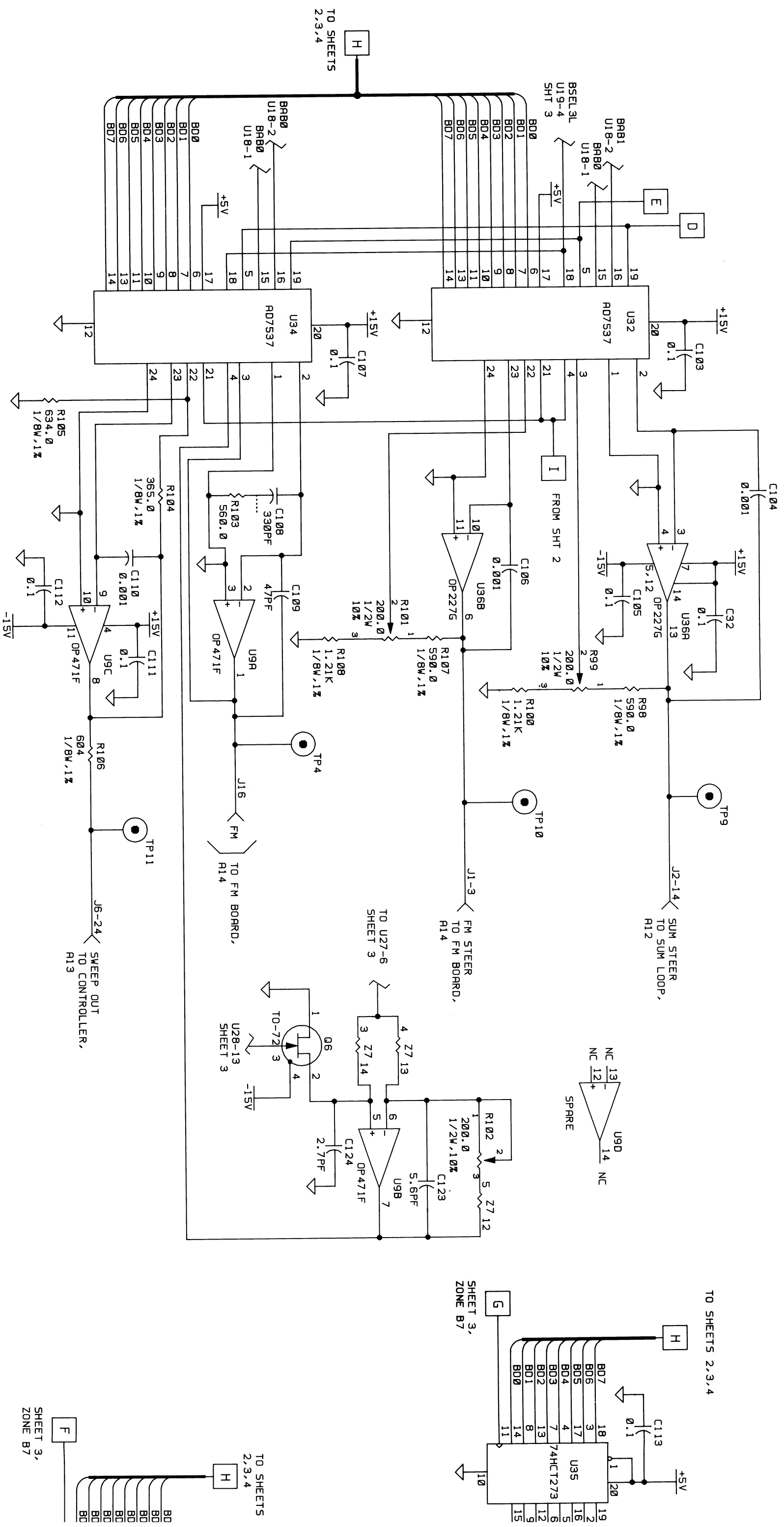


Figure 8-11. A11 Modulation Control PCA (cont)





SCHEMATIC DIAGRAMS

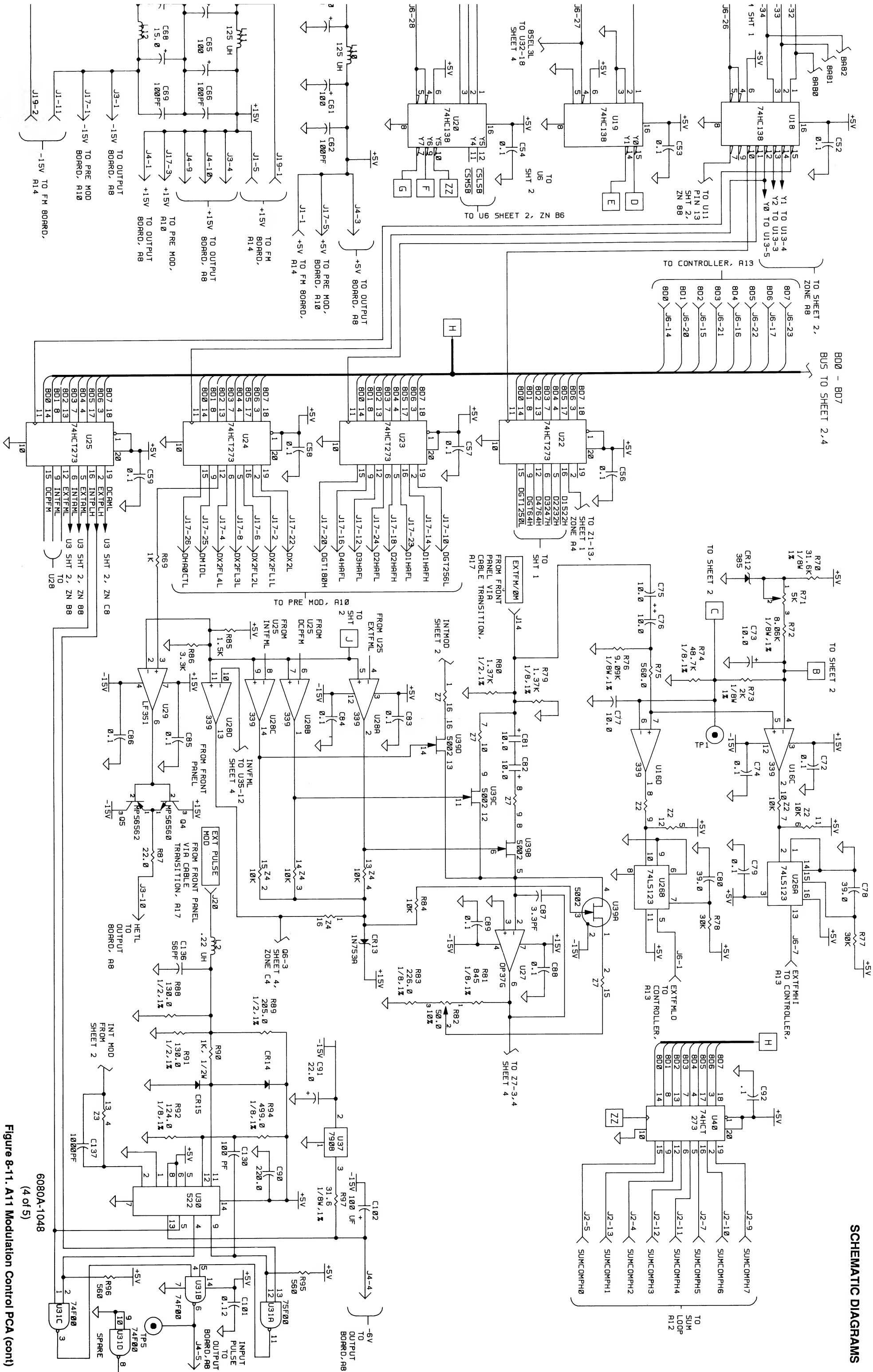
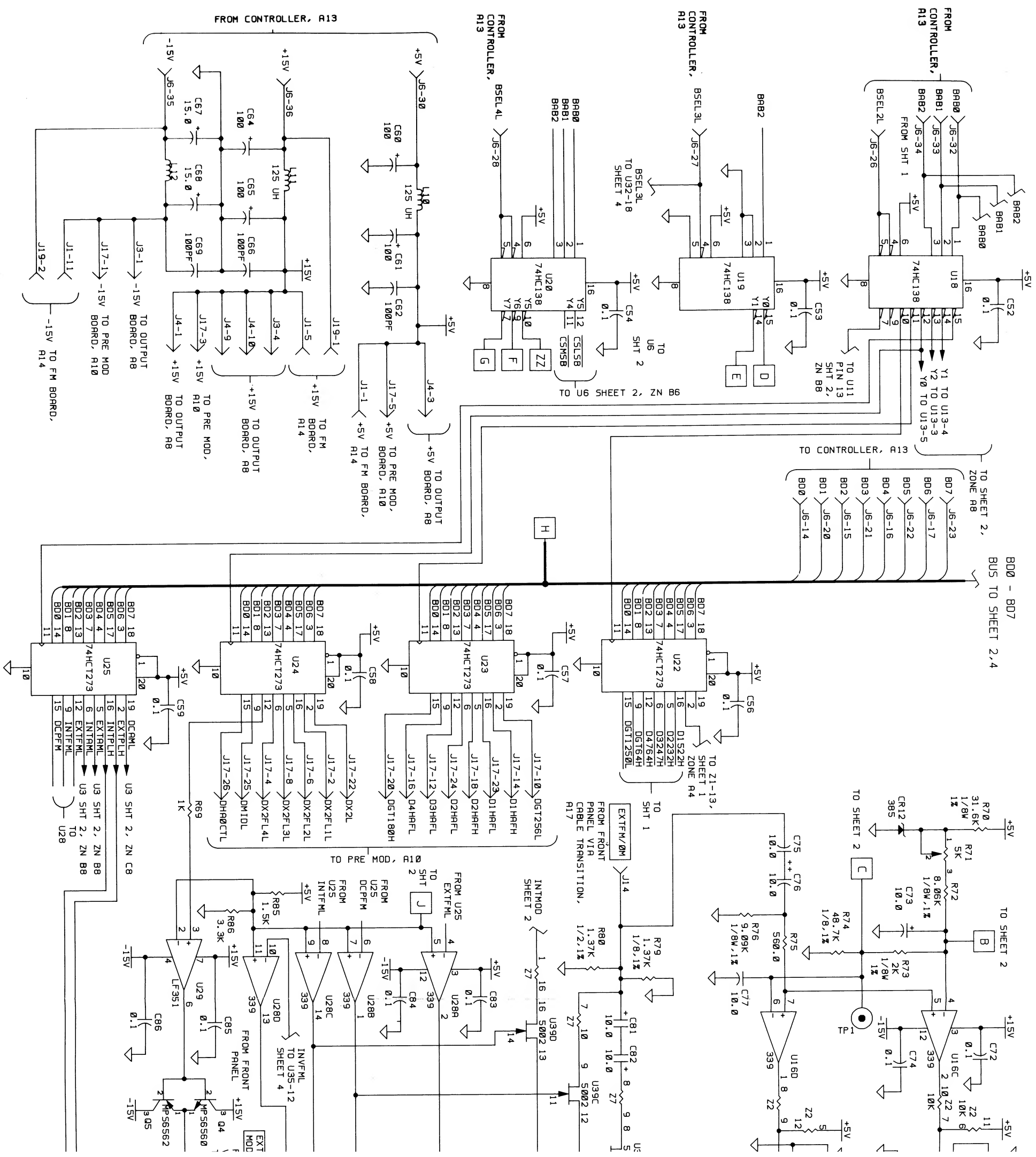
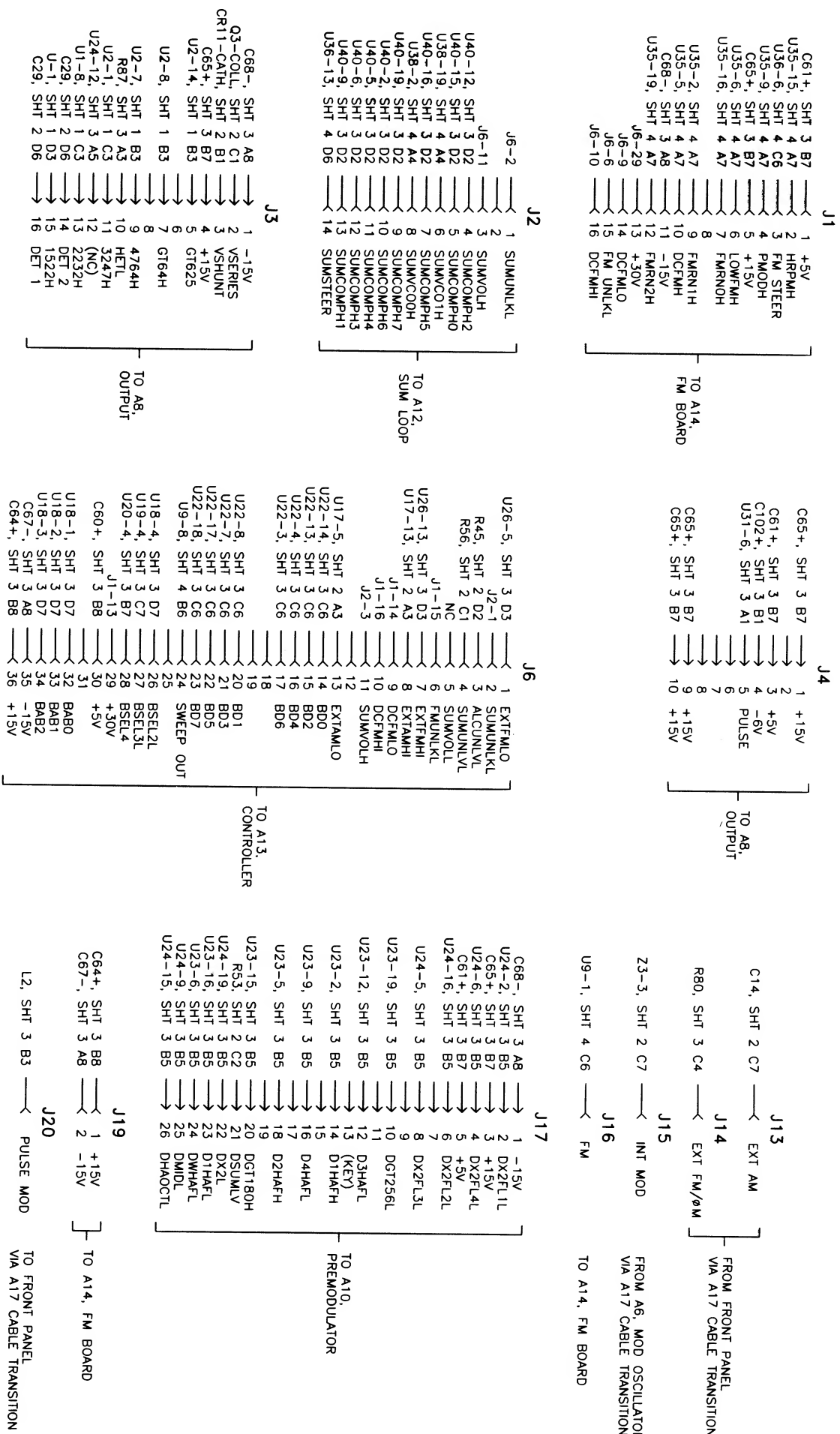


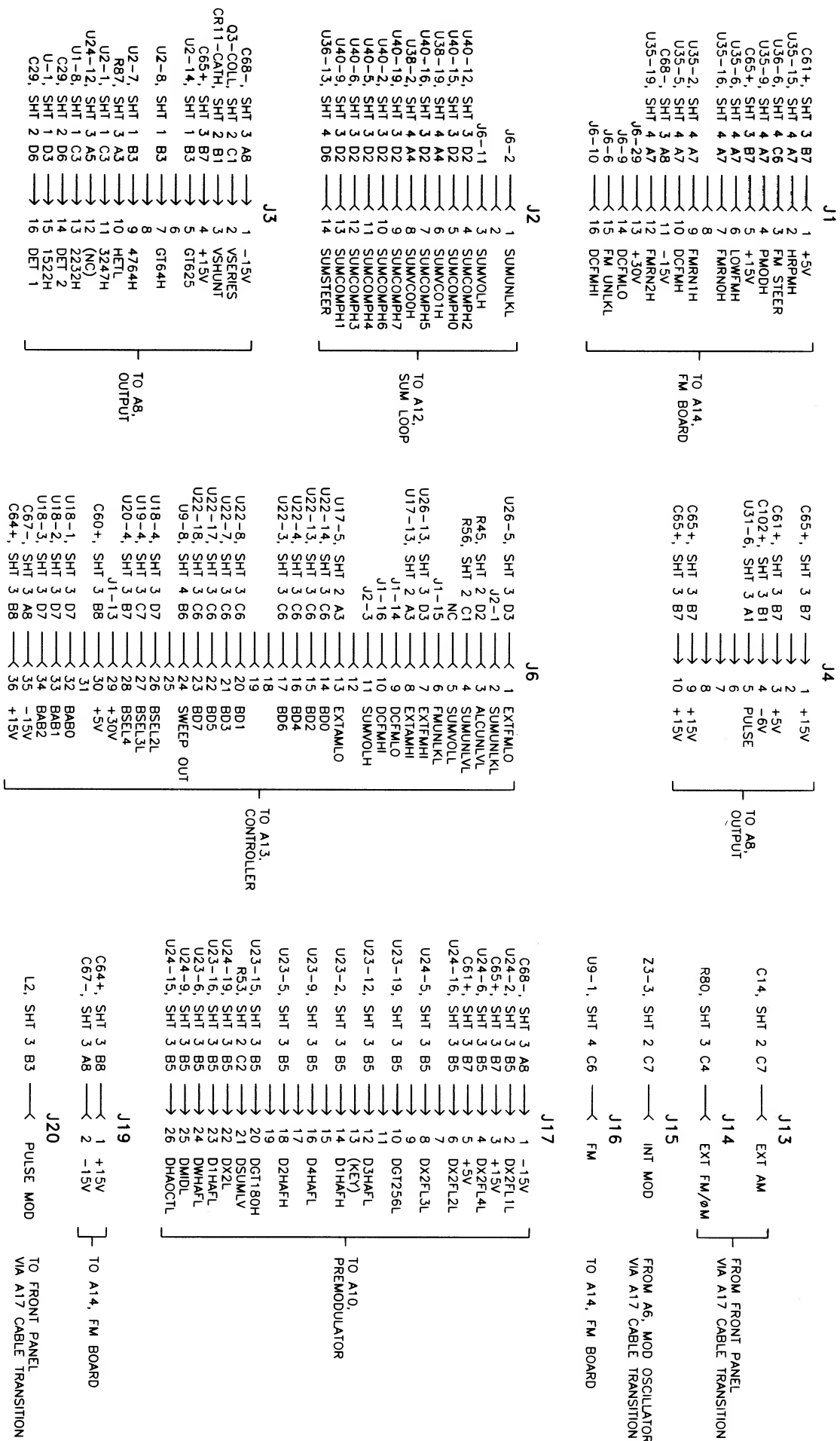
Figure 8-11. A11 Modulation Control PCA (cont)





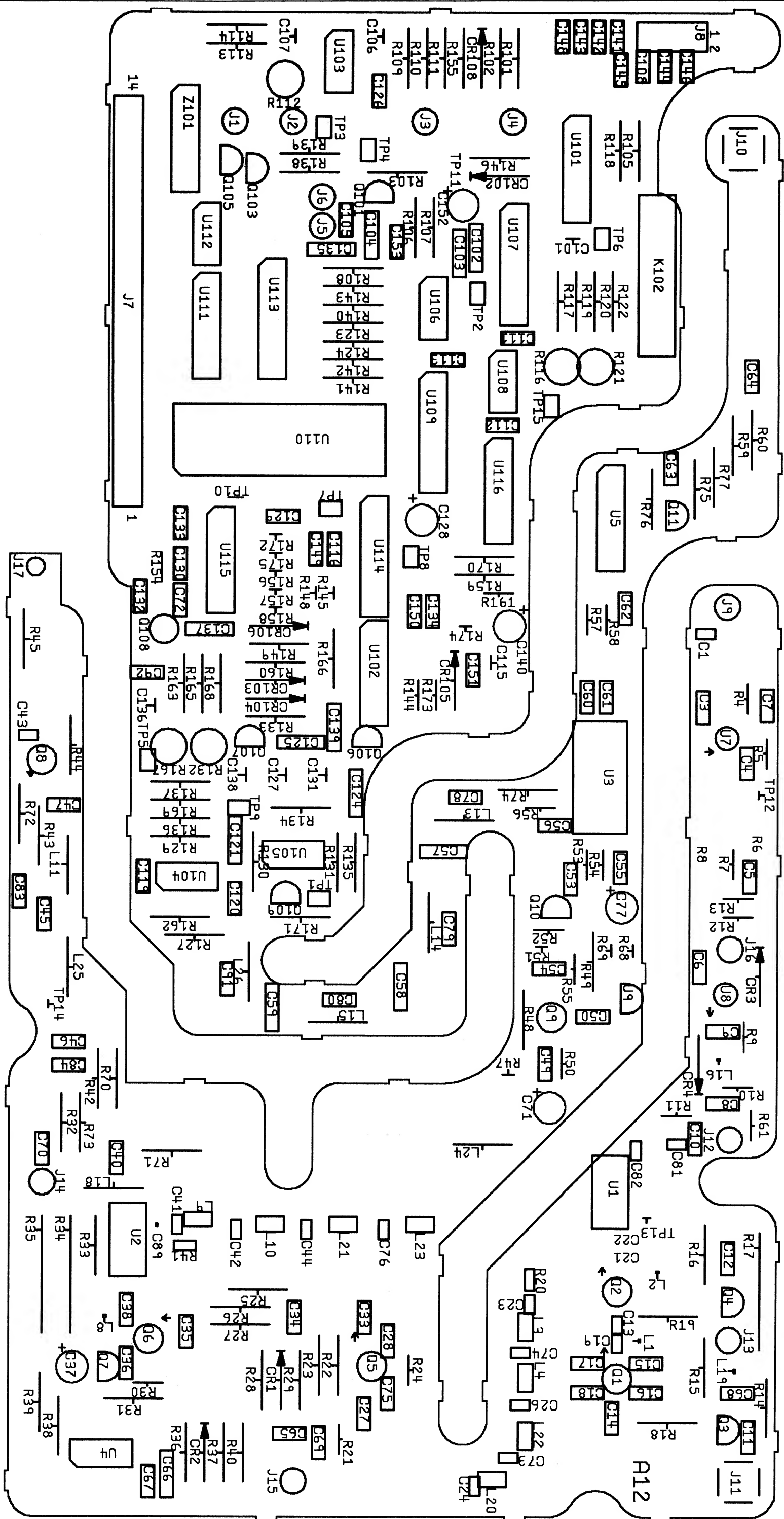
CONNECTOR SUMMARY

Figure 8-11. A11 Modulation Control PCA (cont)



CONNECTOR SUMMARY

Figure 8-11. A11 Modulation Control PCA (cont)



8060A-1607

Figure 8-12. A12 Sum Loop PCA

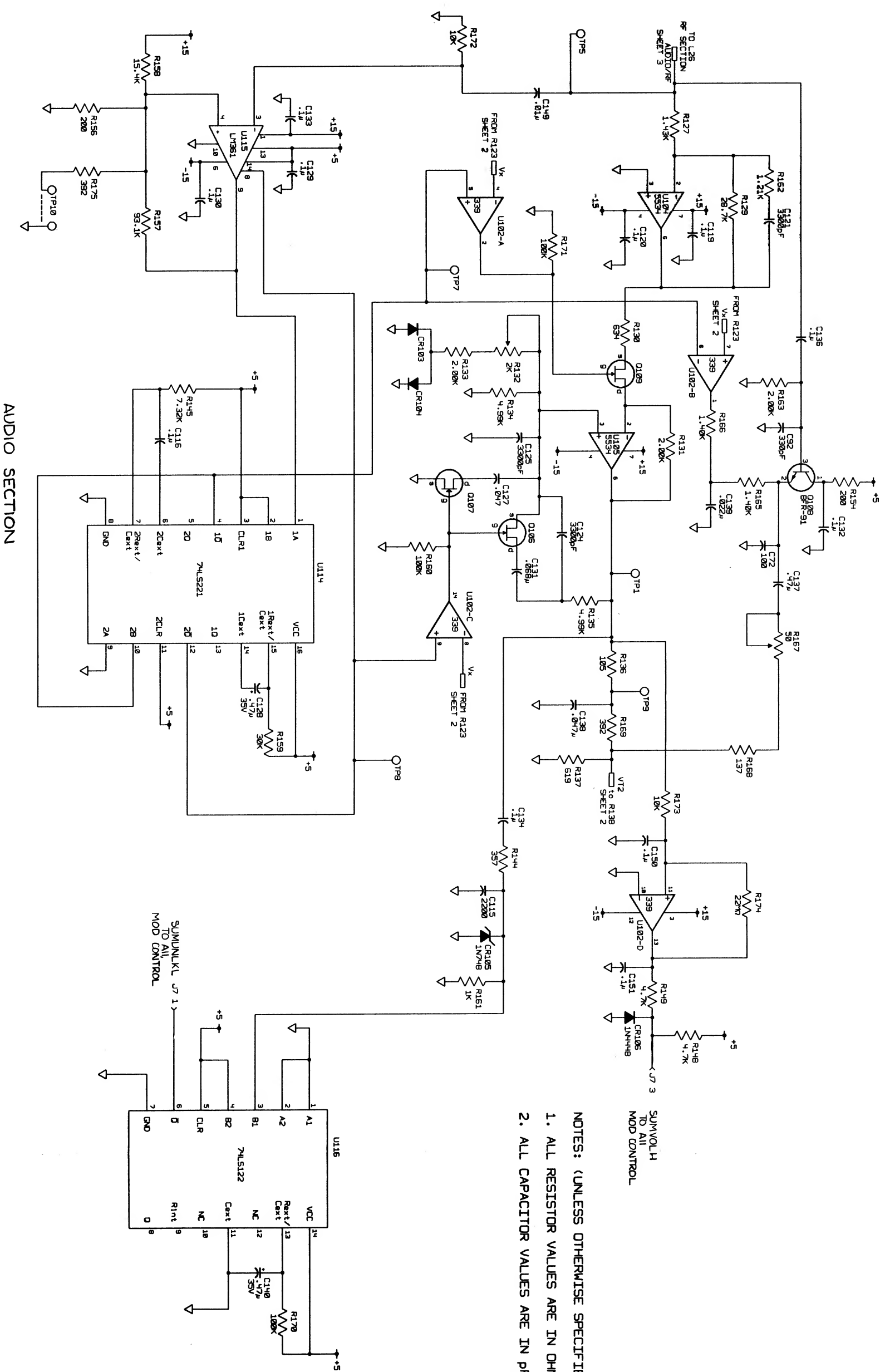


Figure 8-12. A12 Sum Loop PCA (cont)

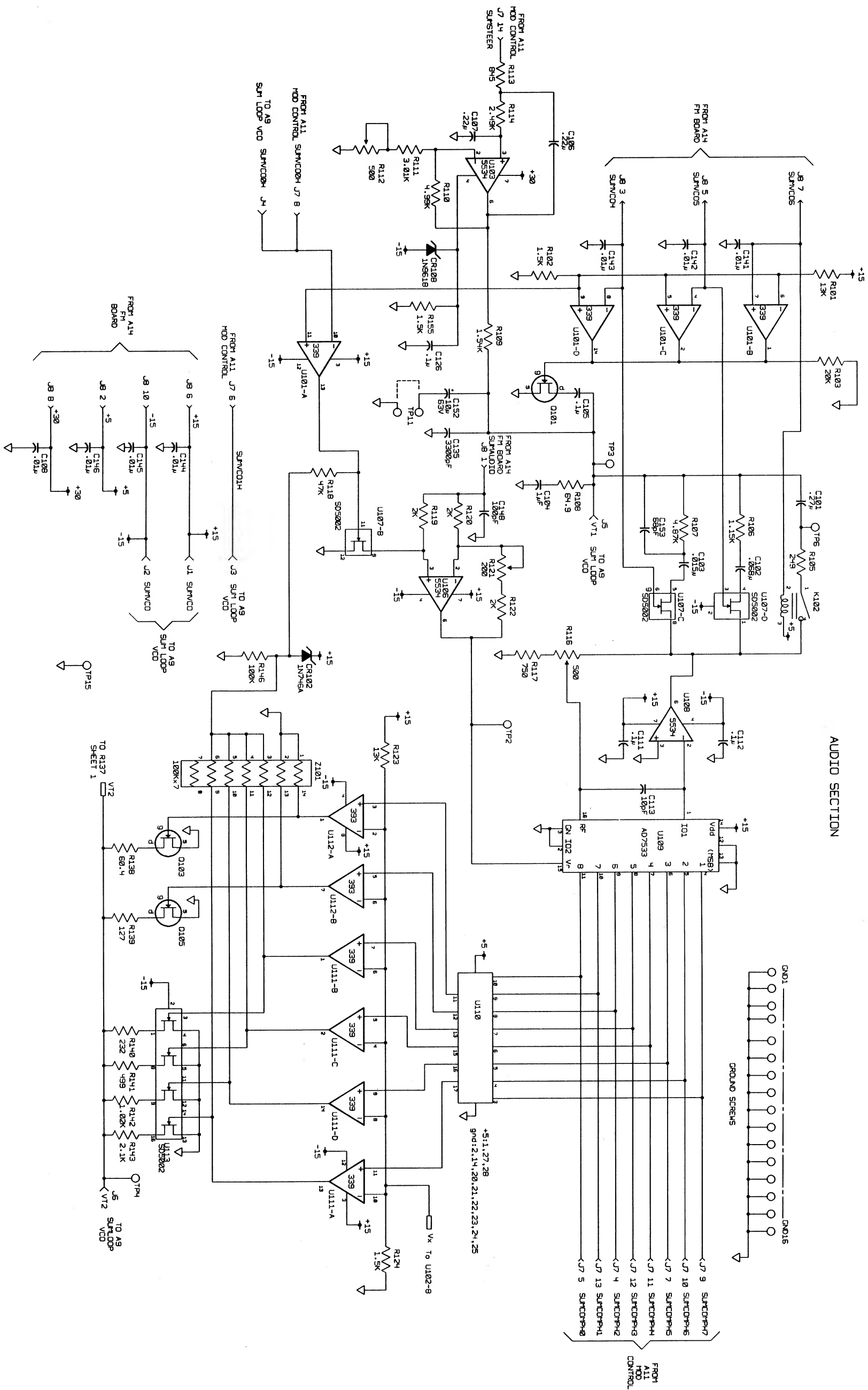
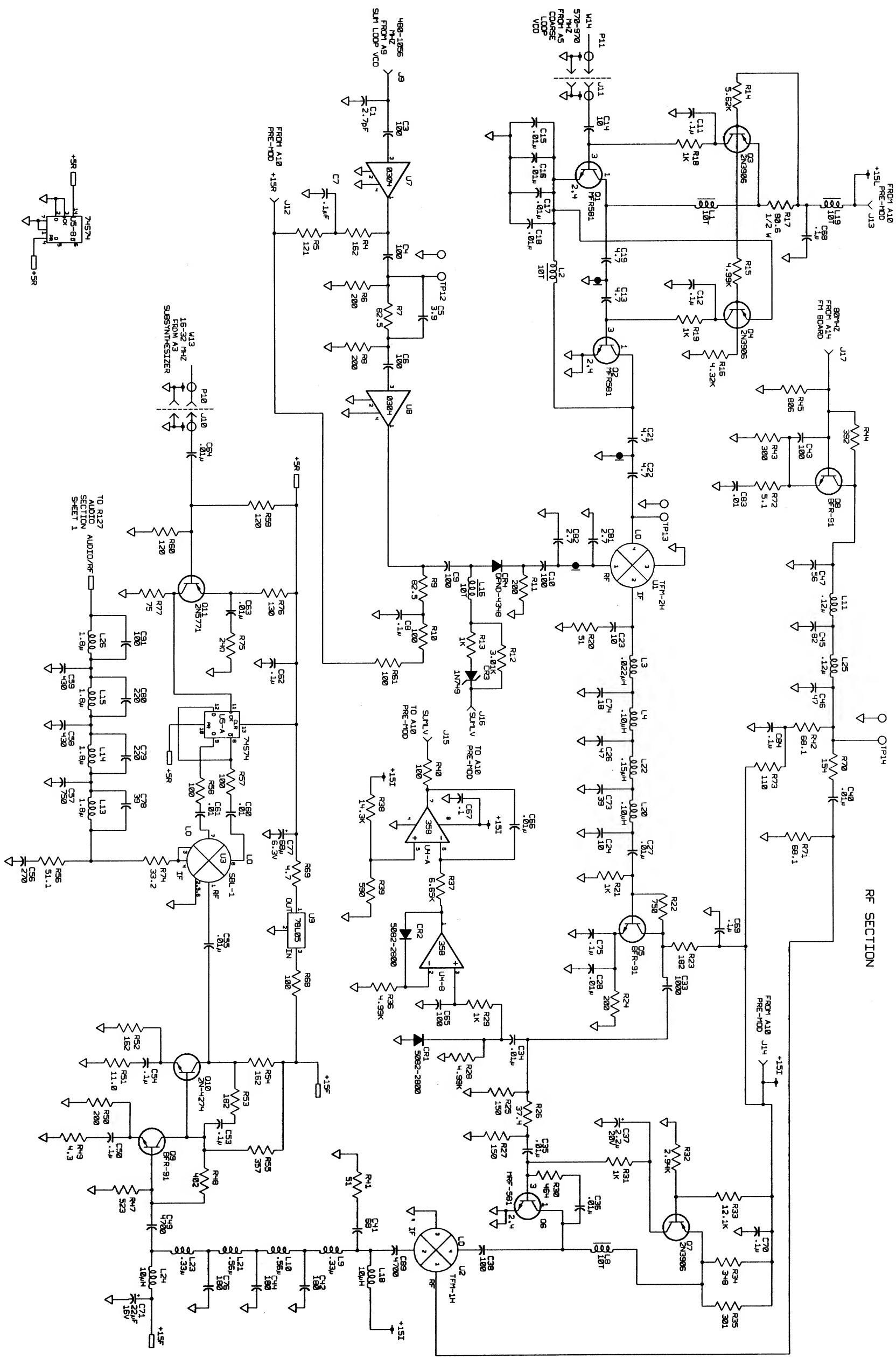


Figure 8-12. A12 Sum Loop PCA (cont)



6080A-1042
(3 of 3)

Figure 8-12. A12 Sum Loop PCA (cont)

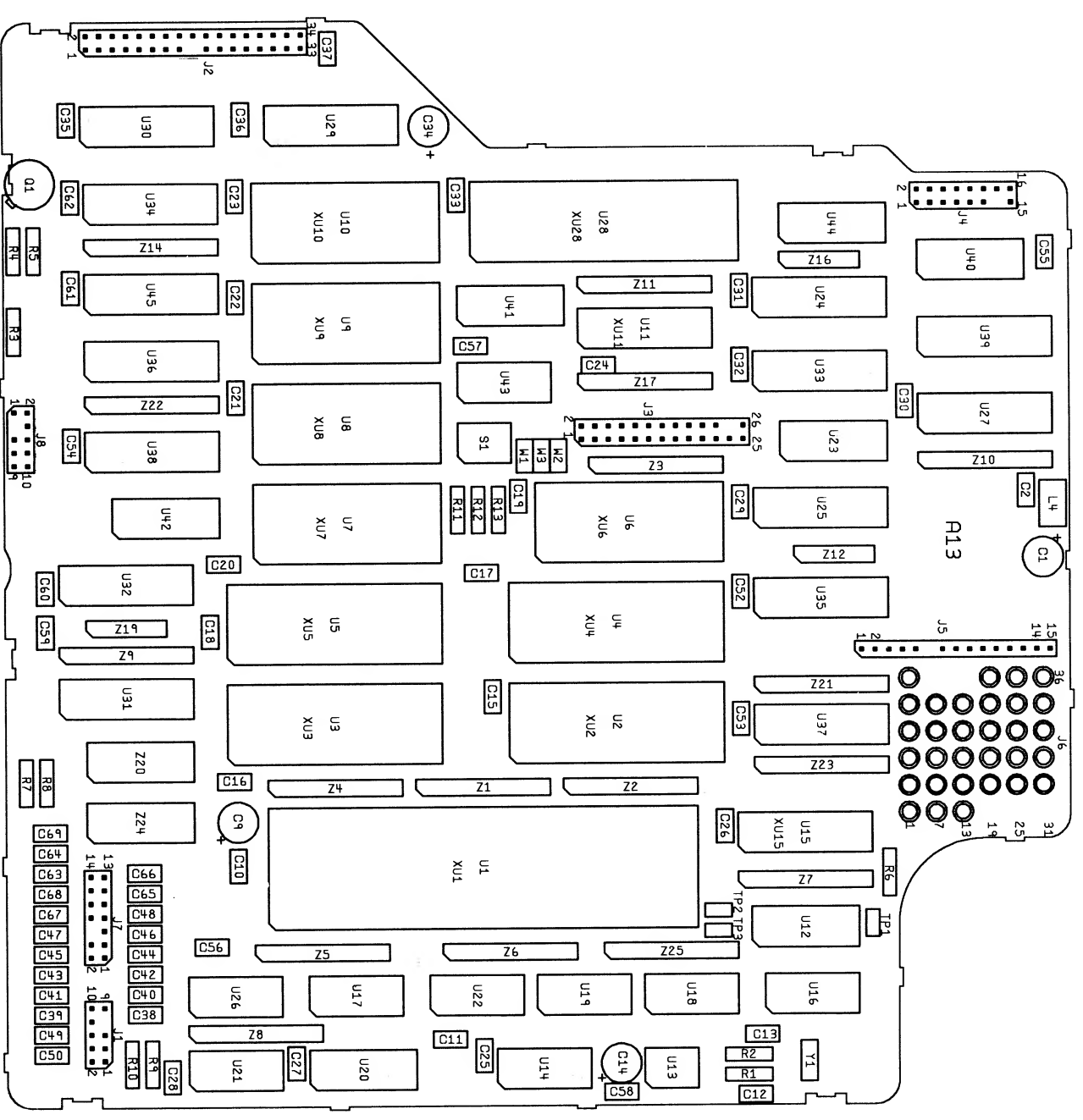


Figure 8-13. A13 Controller PCA

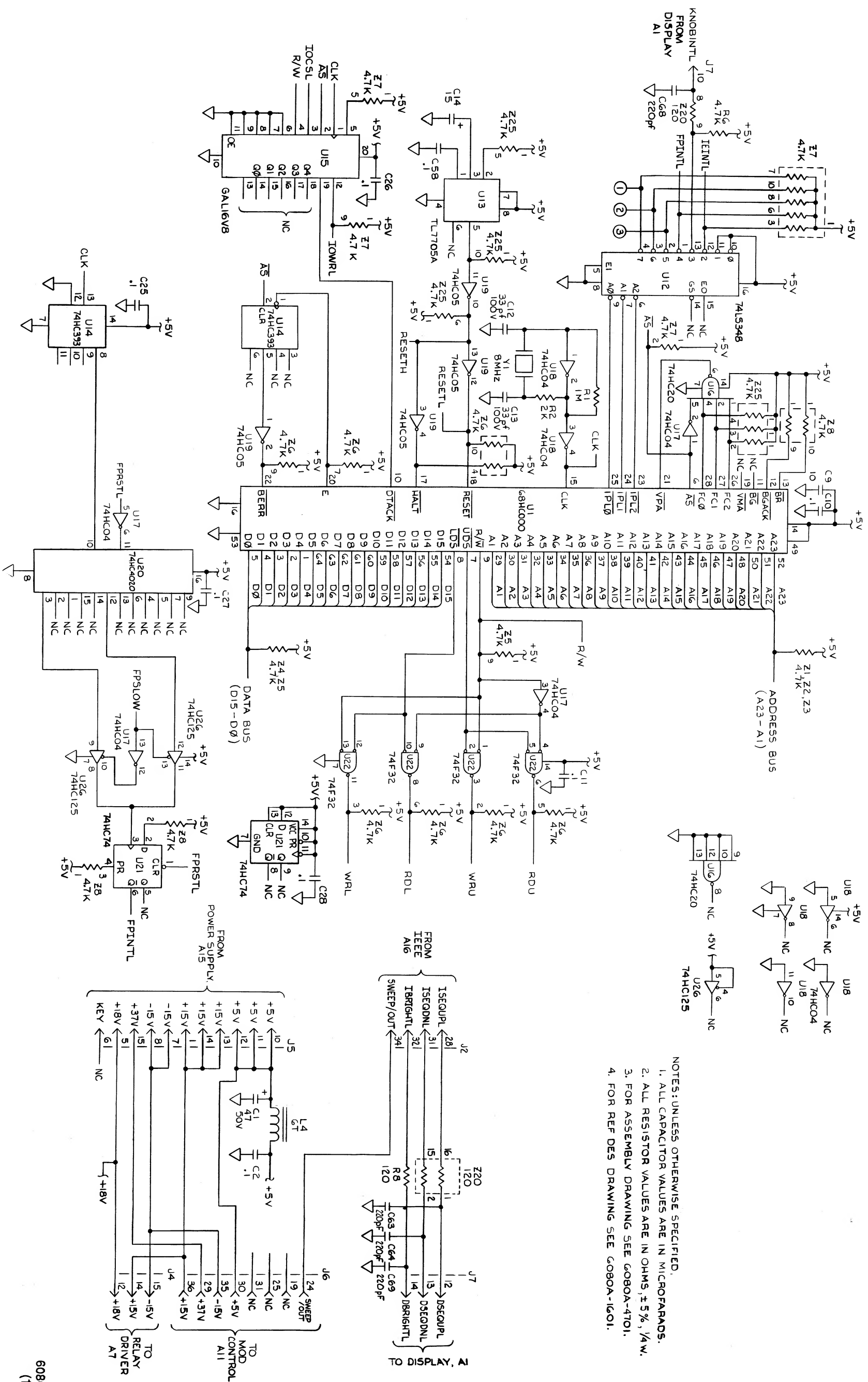


Figure 8-13. A13 Controller PCA (cont)

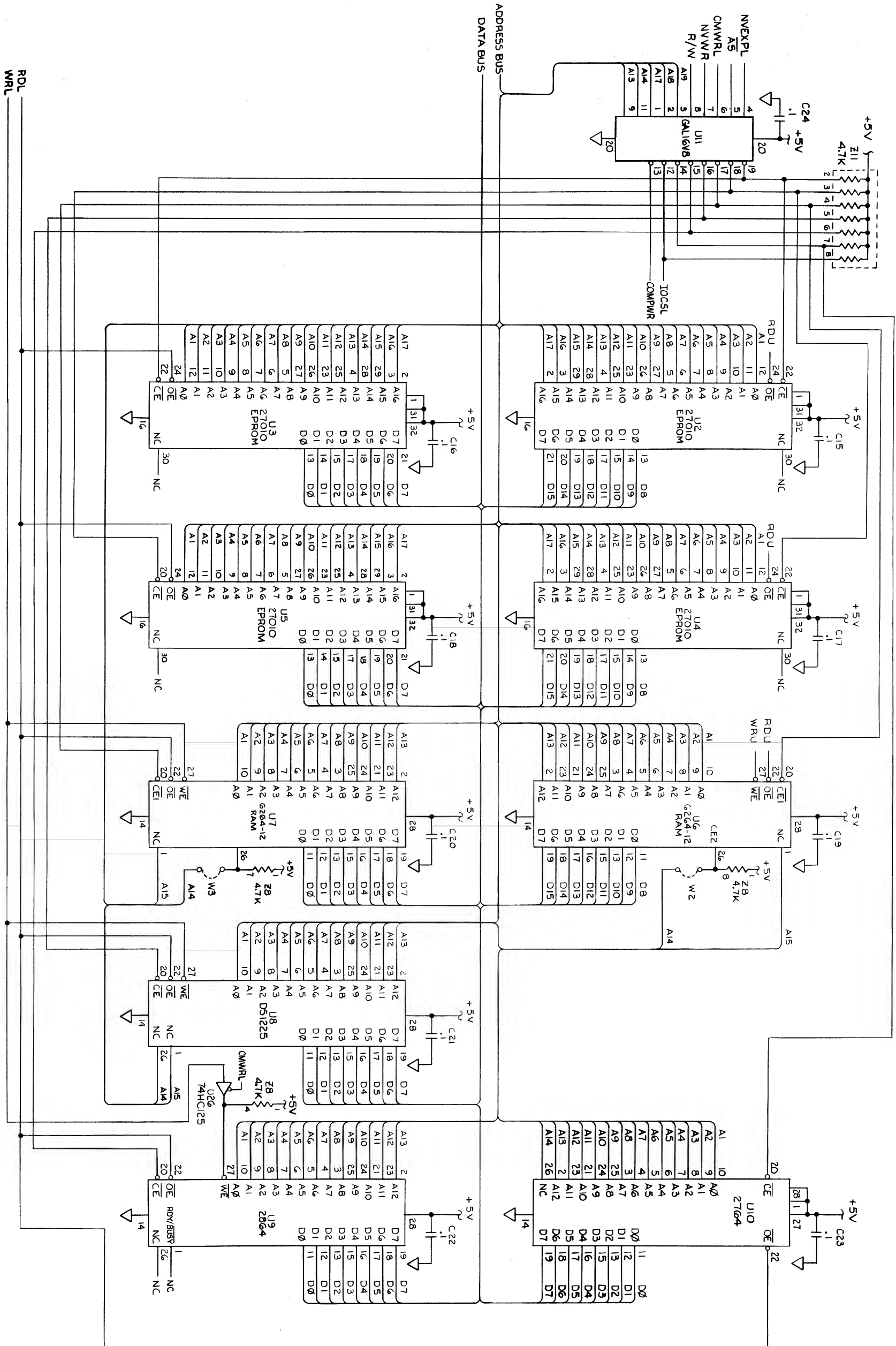


Figure 8-13. A13 Controller PCA (cont)

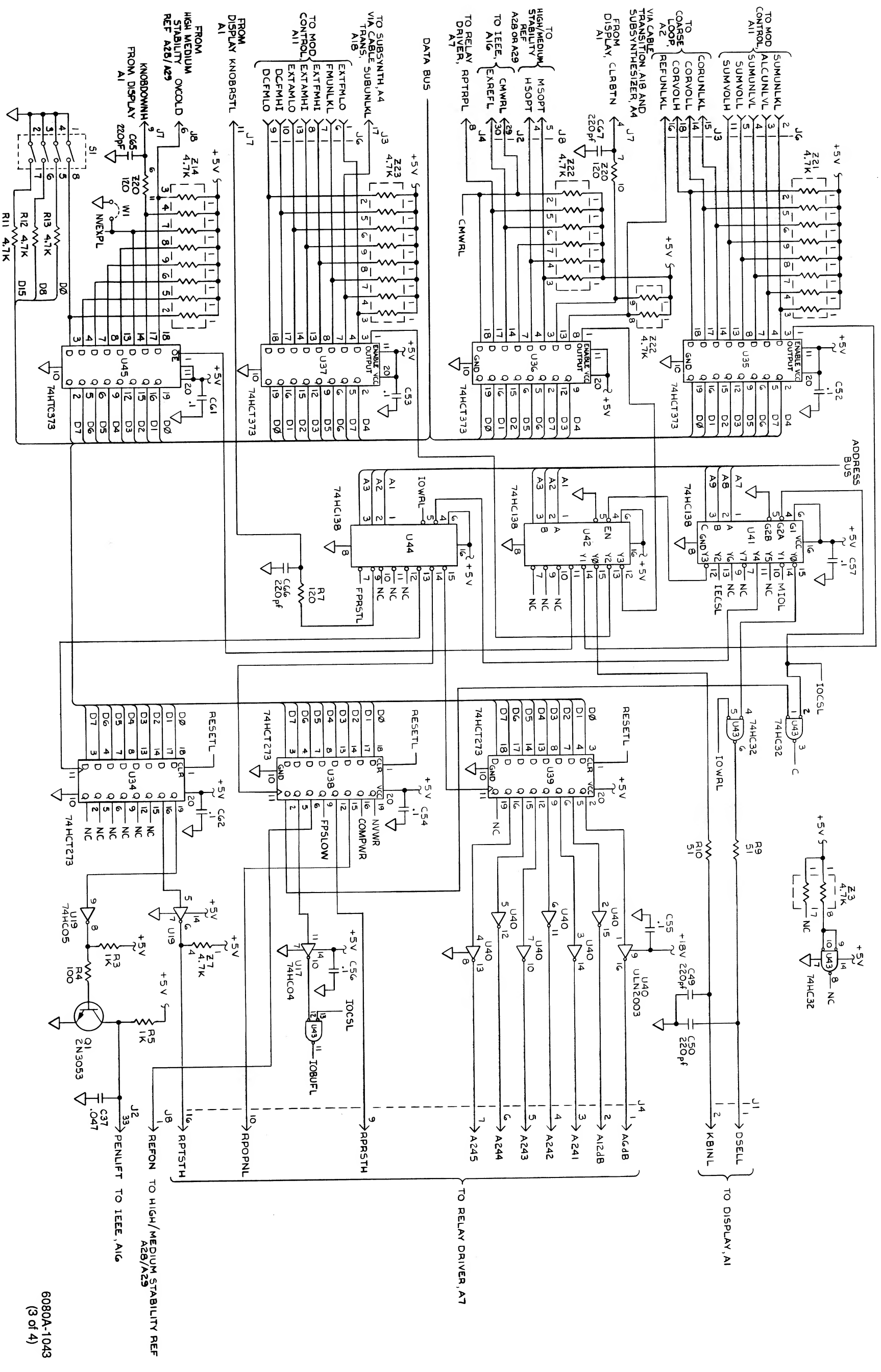


Figure 8-13. A13 Controller PCA (cont)

SCHEMATIC DIAGRAMS

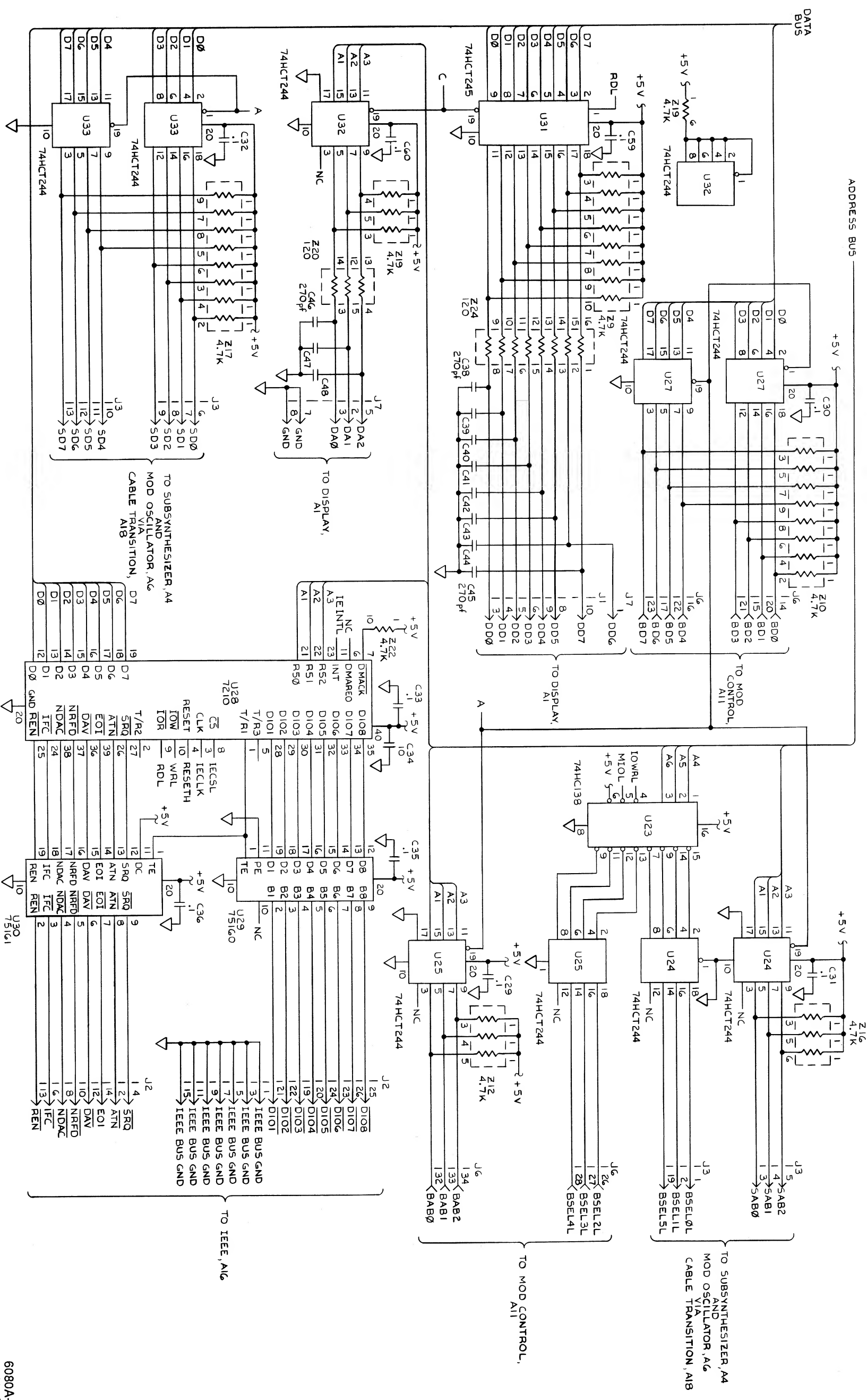


Figure 8-13. A13 Controller PCA (cont)

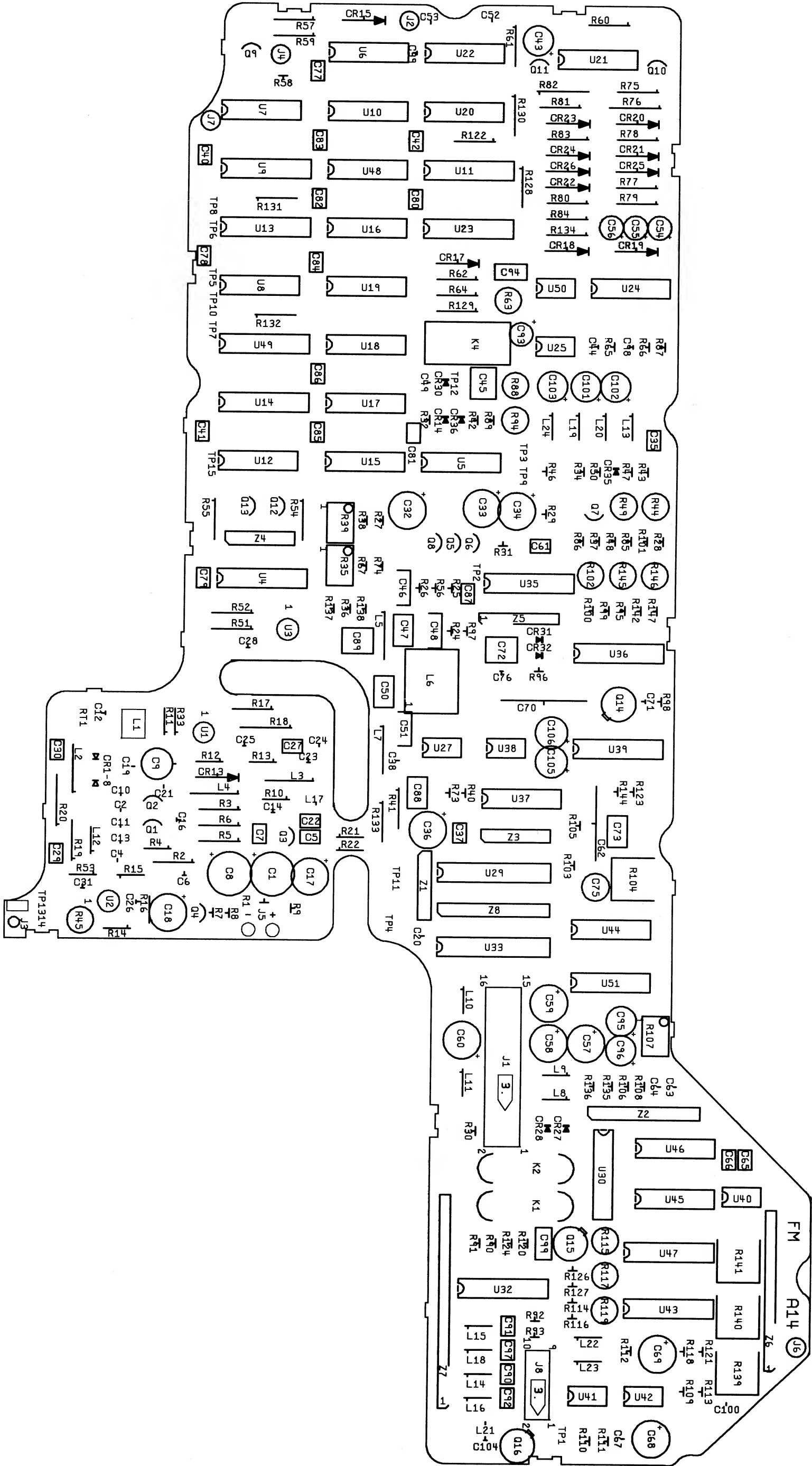


Figure 8-14. A14 FM PCA

6080A-1606

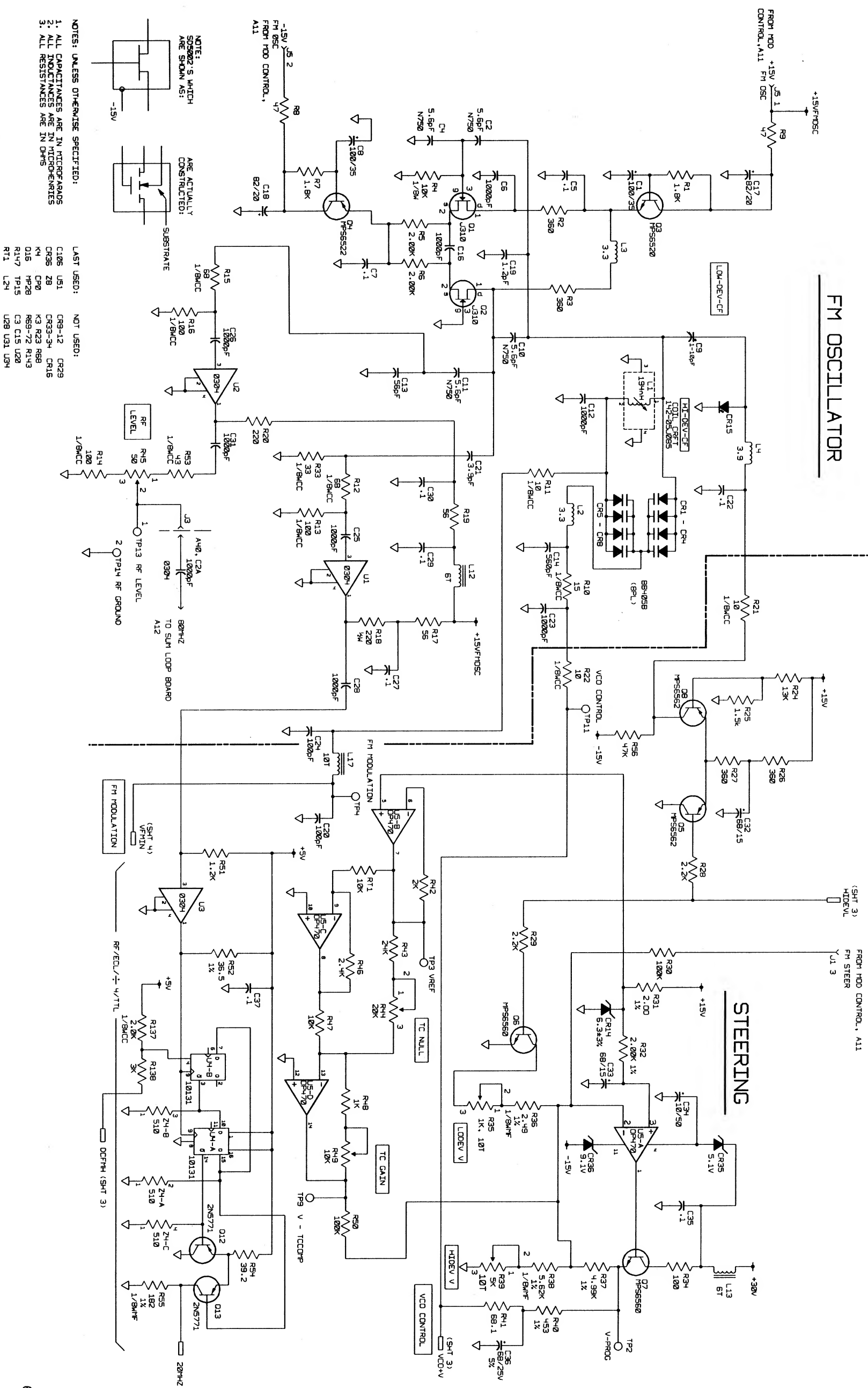


Figure 8-14. A14 FM PCA (cont)

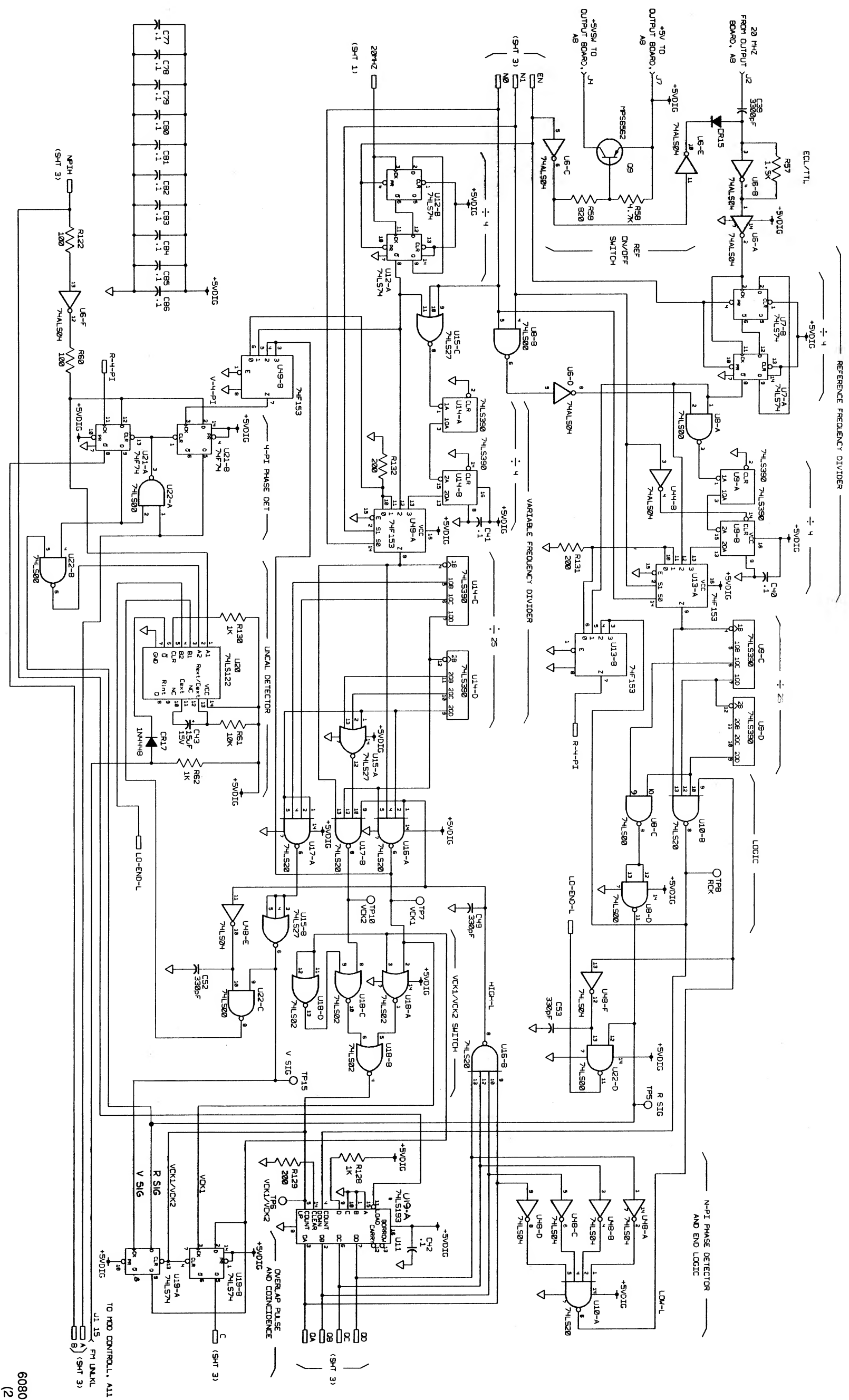


Figure 8-14. A14 FM PCA (cont)

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(2 of 4)

SCHEMATIC DIAGRAMS

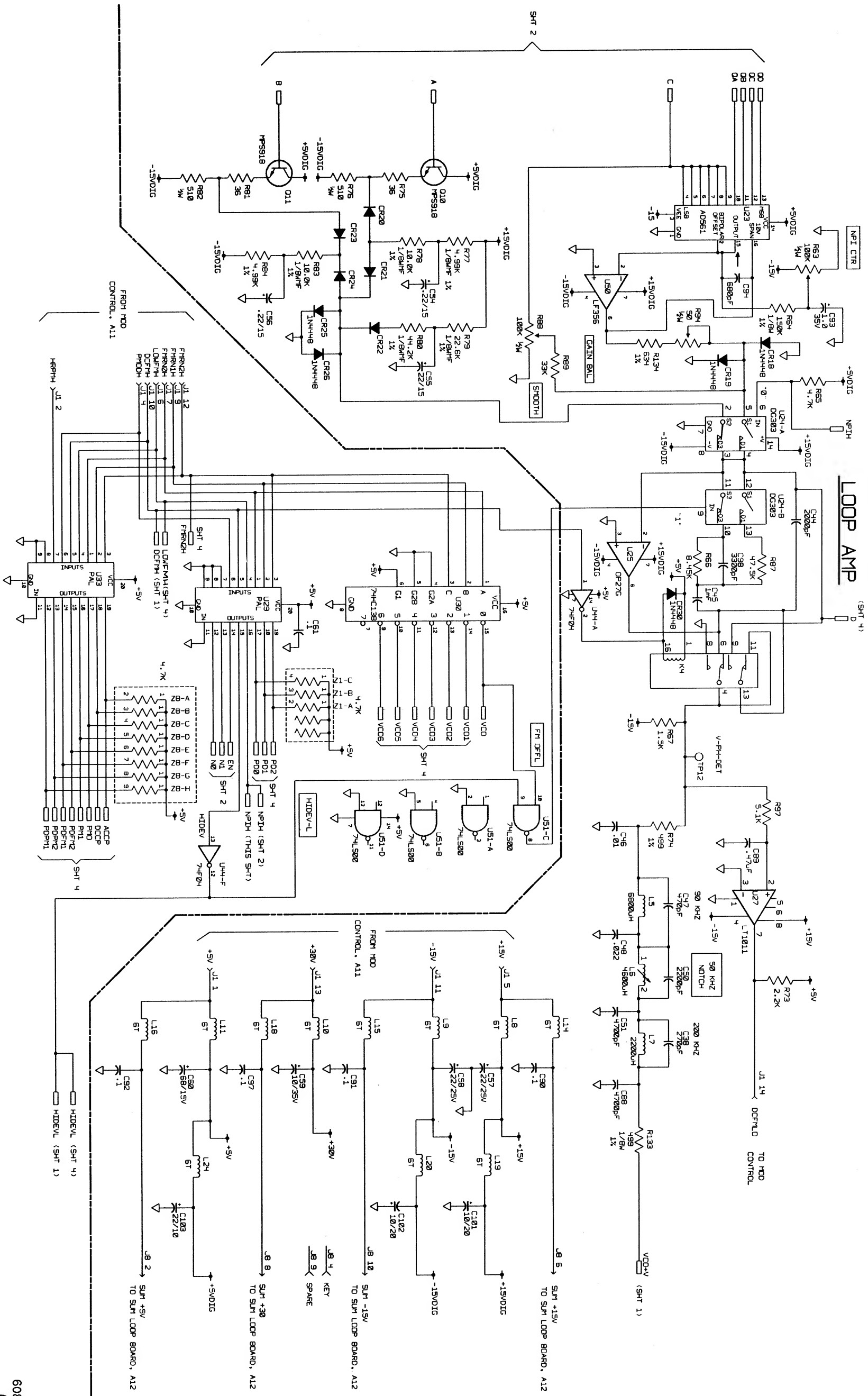


Figure 8-14. A14 FM PCA (cont)

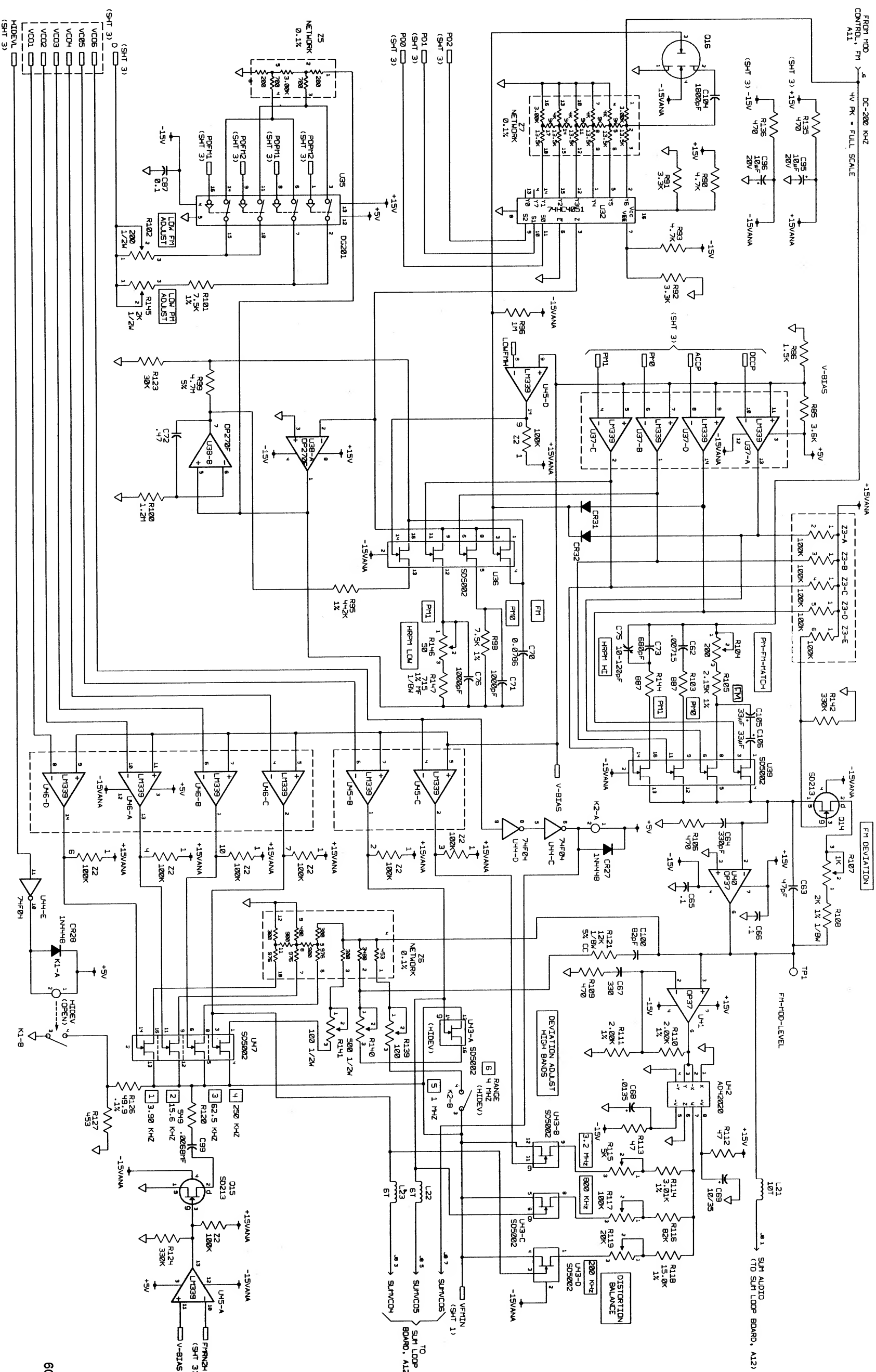


Figure 8-14. A14 FM PCA (cont)

6080A-1045
(4 of 4)

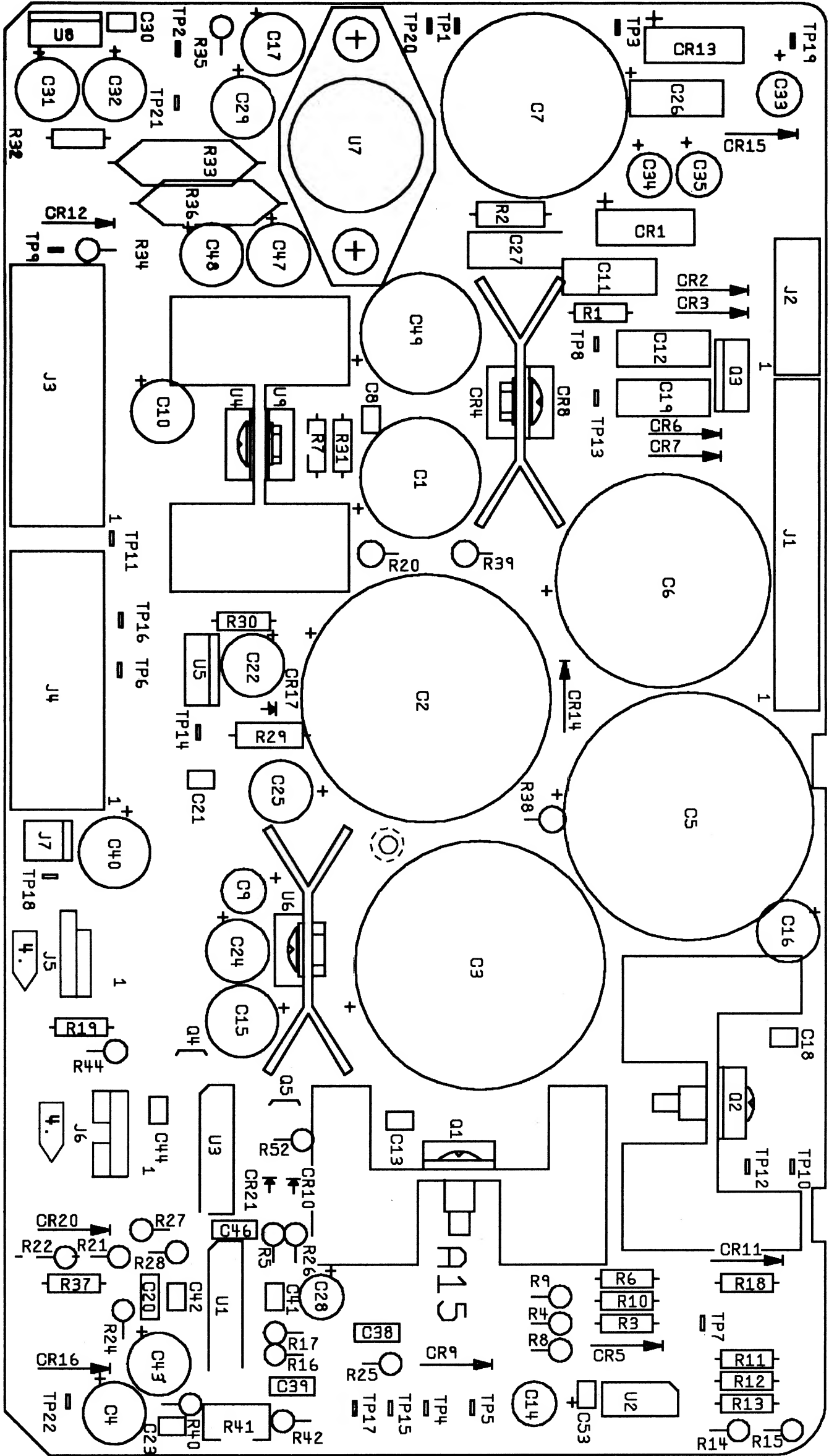


Figure 8-15. A15 Power Supply PCA

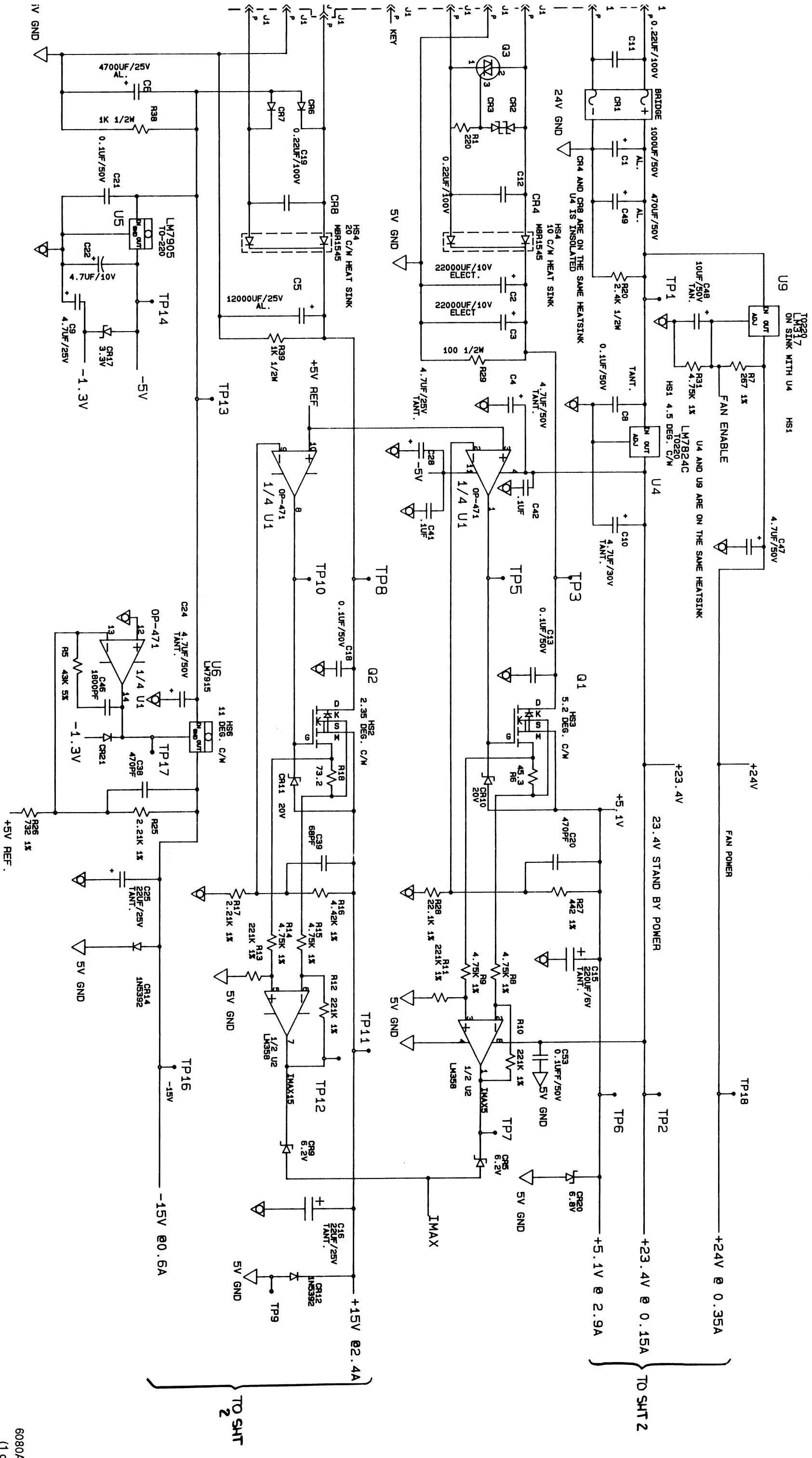
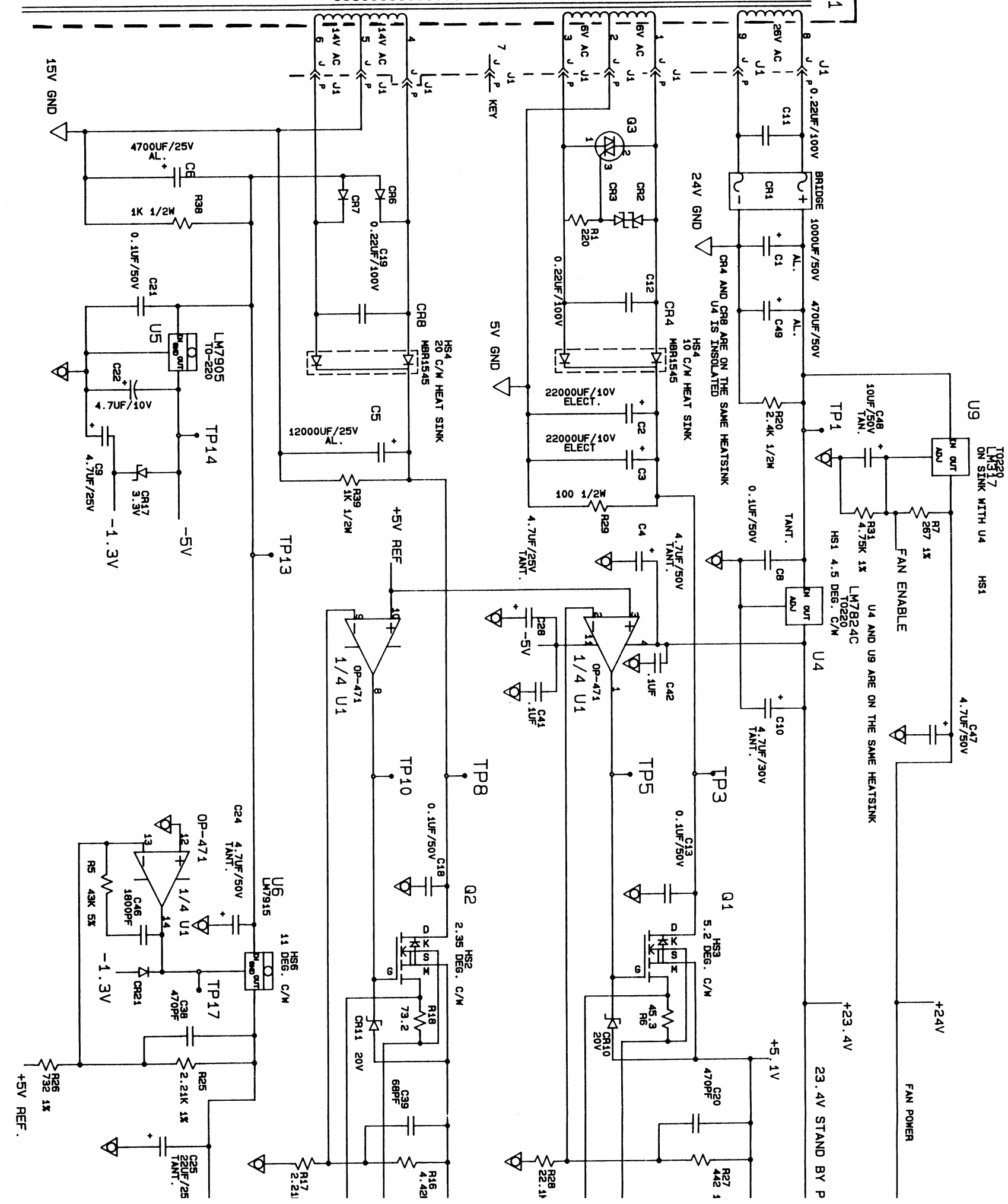
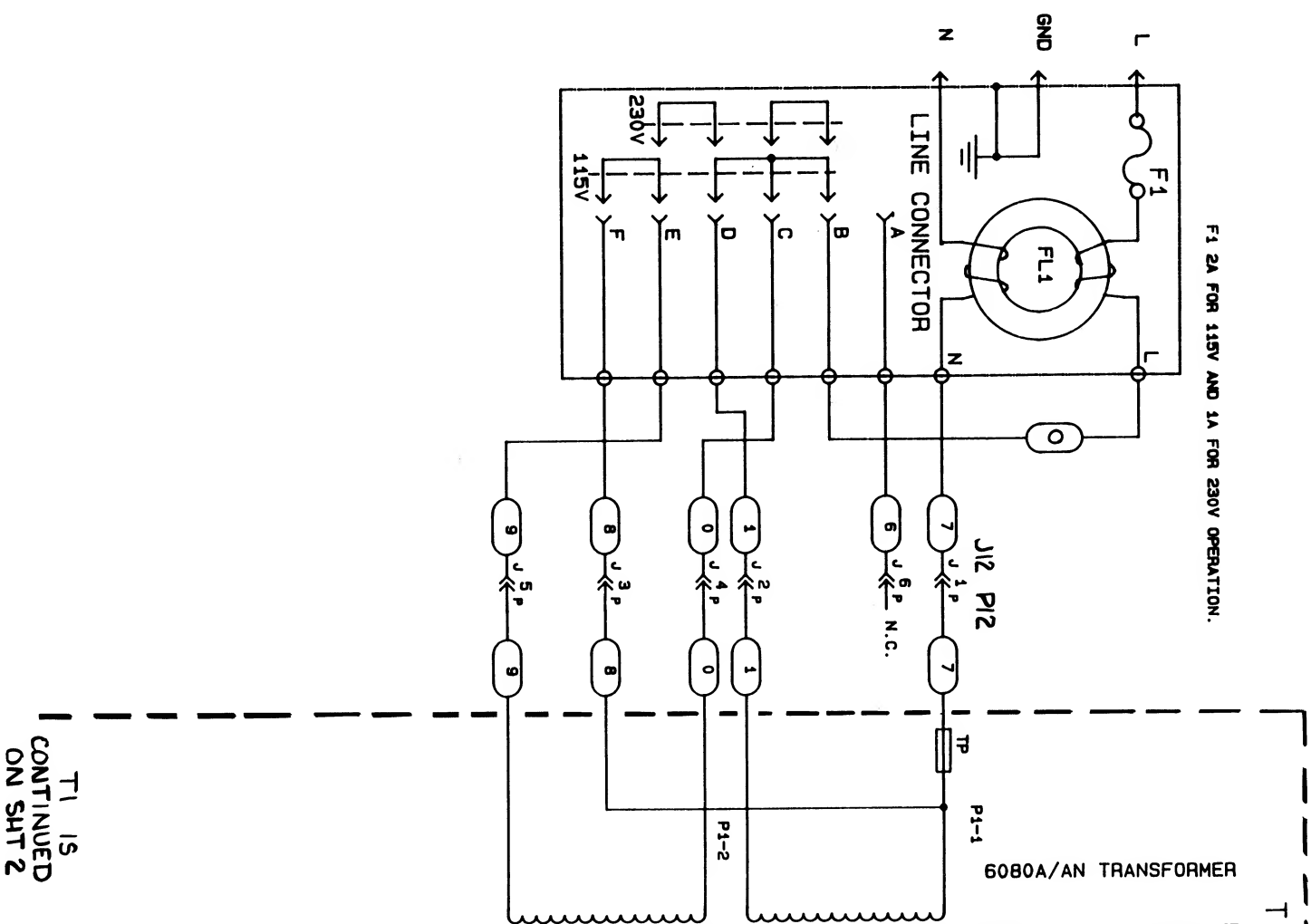


Figure 8-15. A15 Power Supply PCA (cont)



SCHEMATIC DIAGRAMS

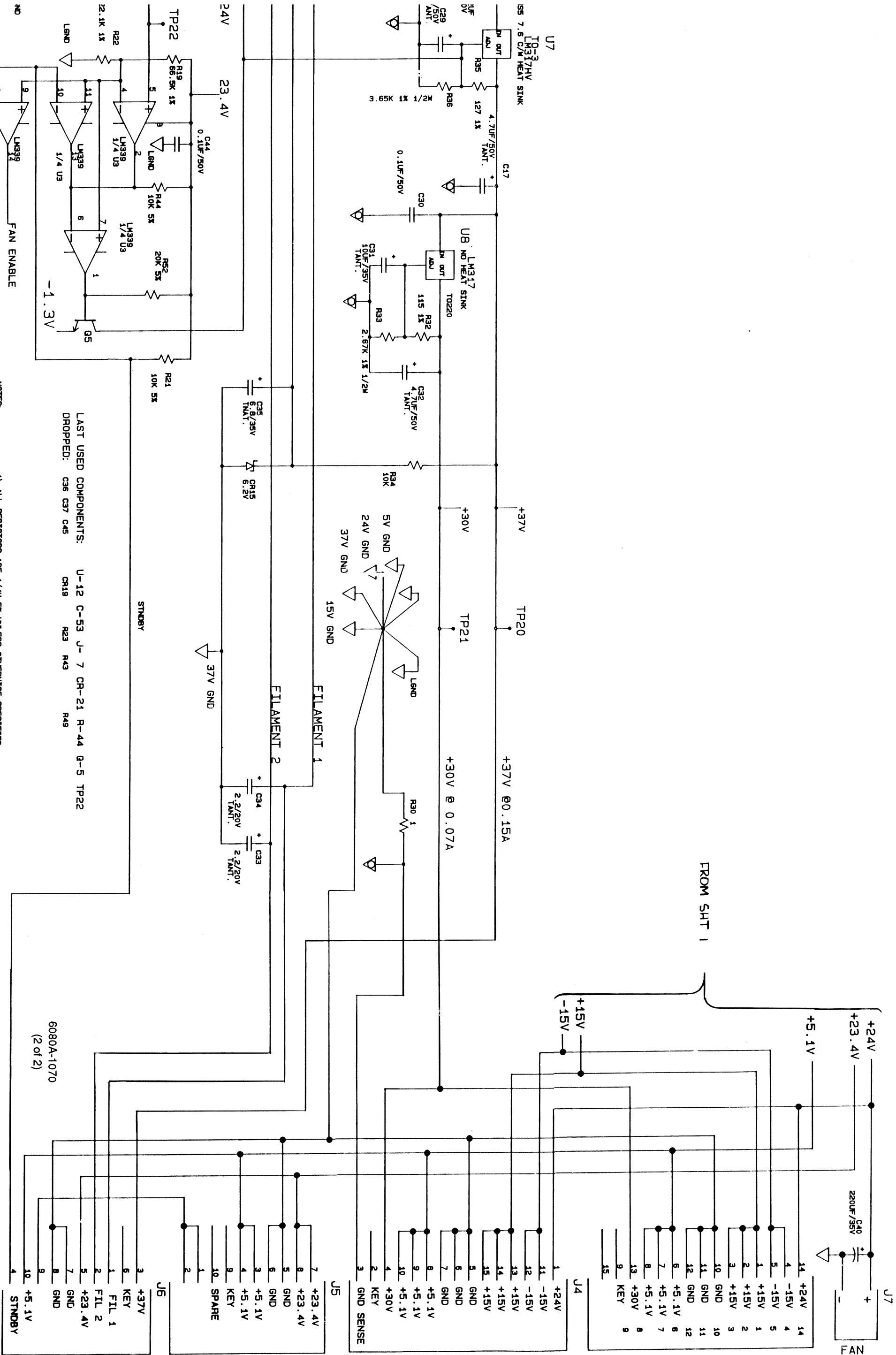


Figure 8-15. A15 Power Supply PCA (cont)

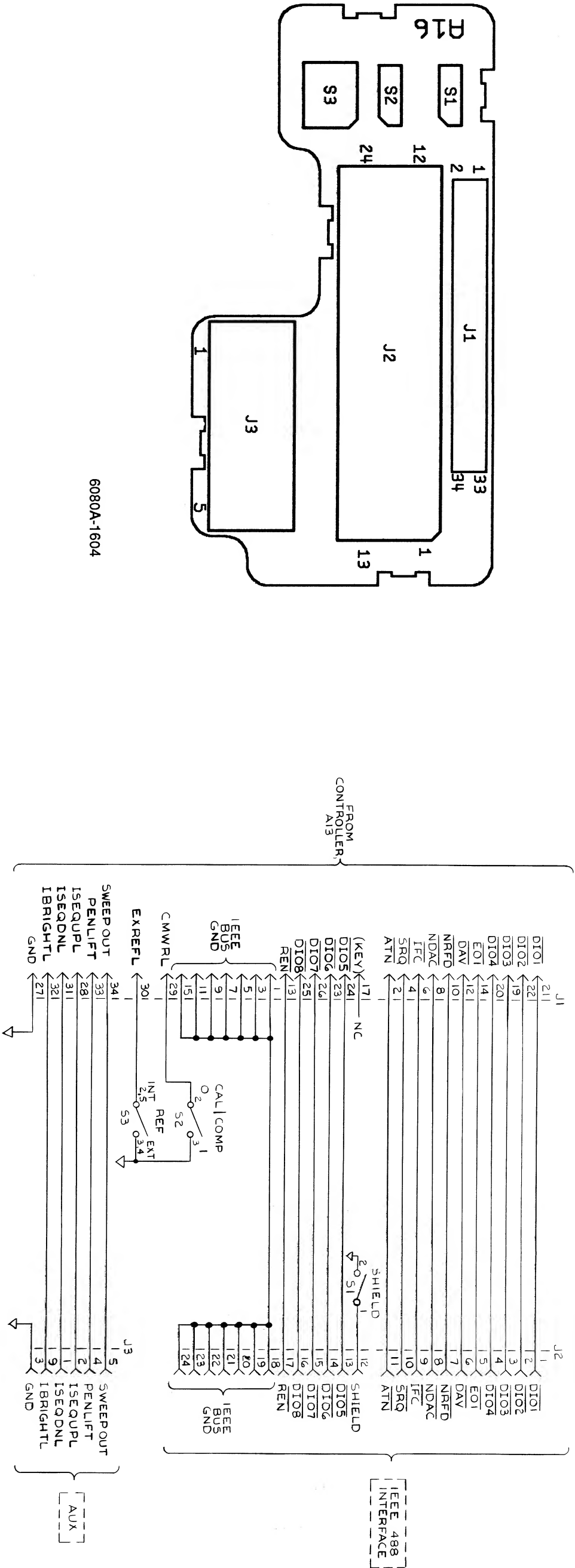
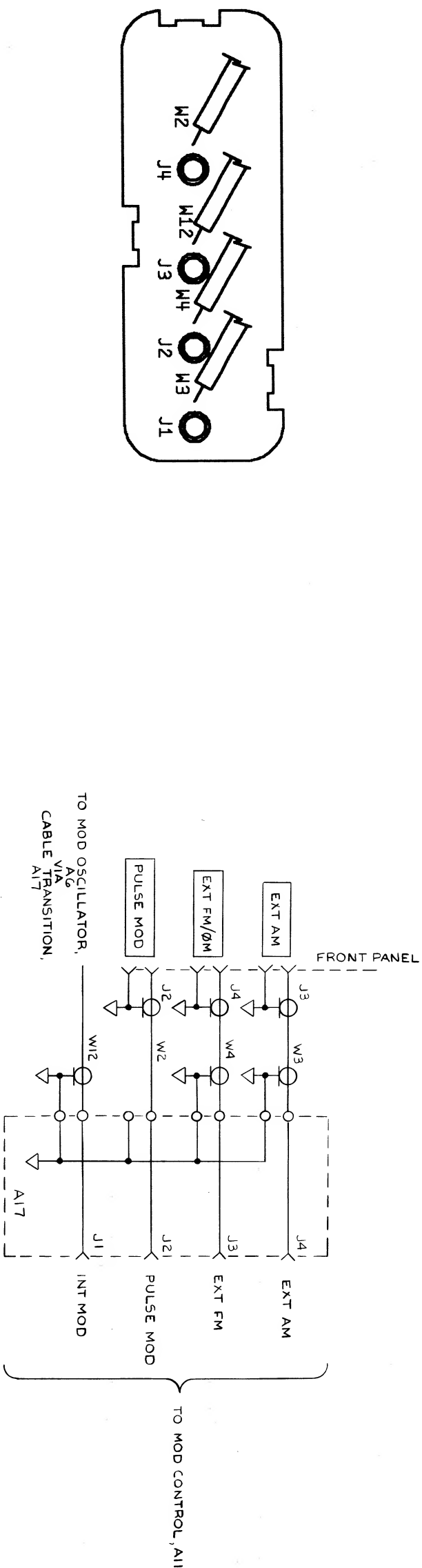


Figure 8-16. A16 IEEE Connector PCA

6080A-1071



6080A-1610

6080A-1044

Figure 8-17. A17 Cable Transition PCA

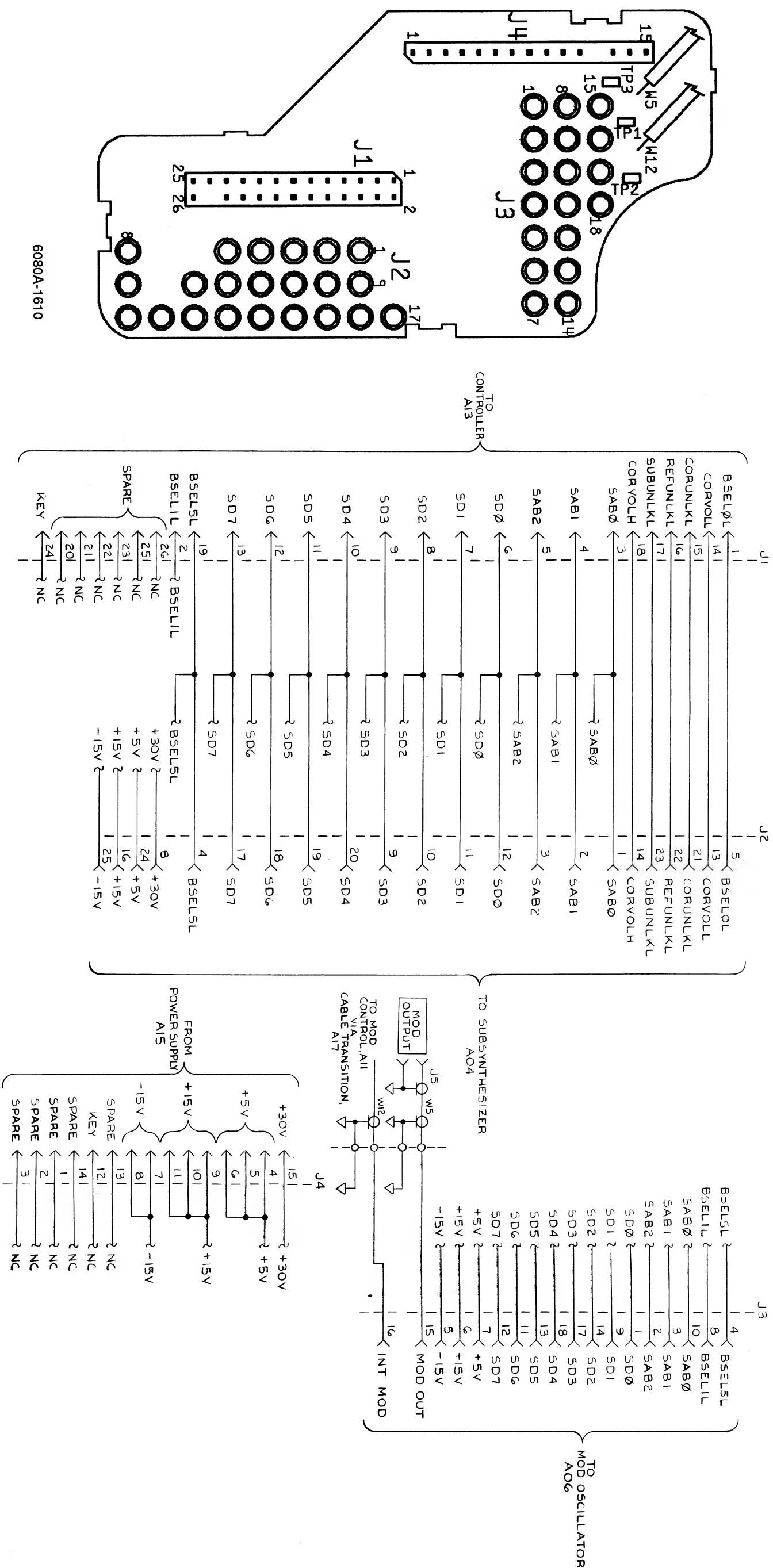
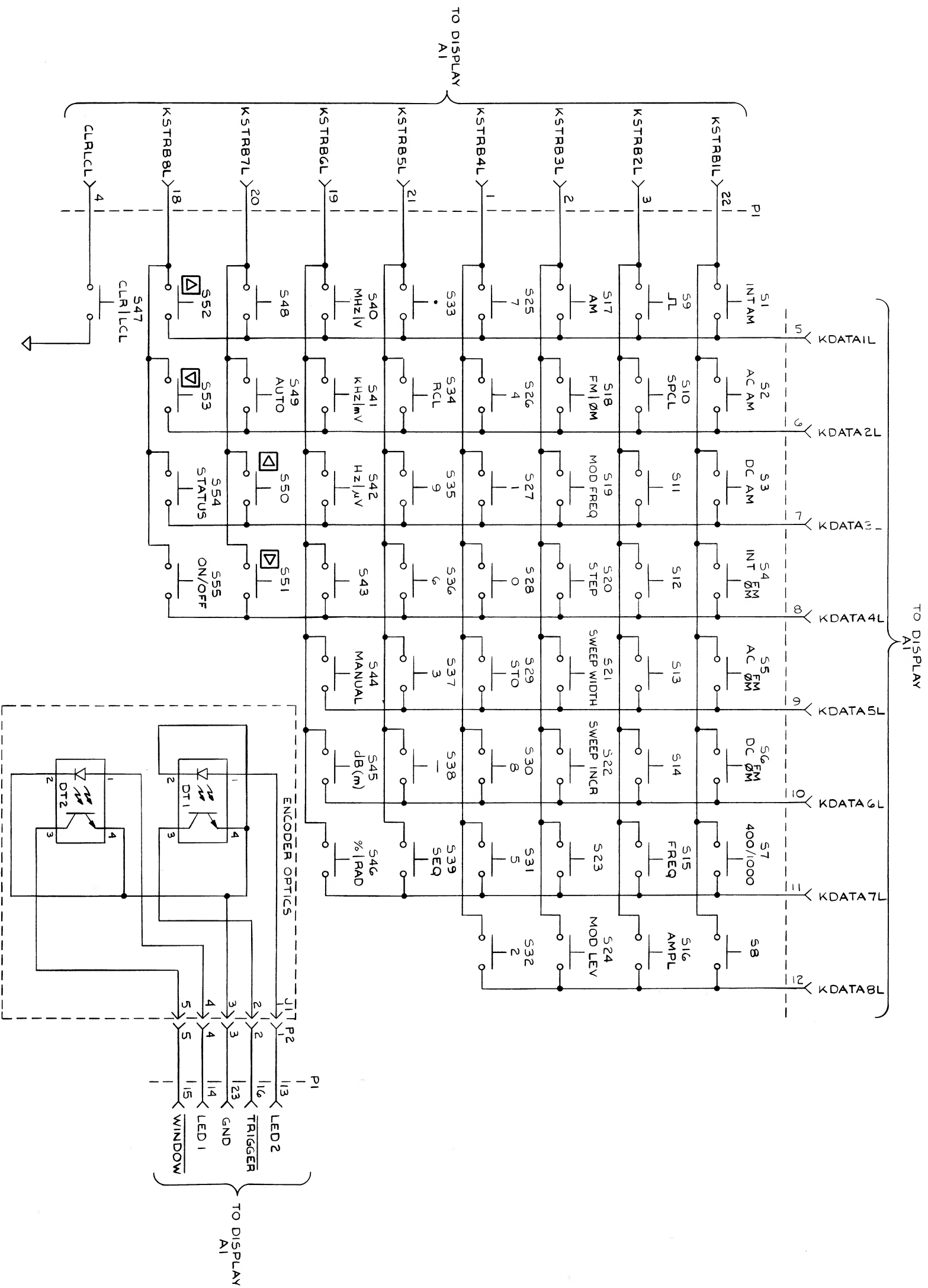
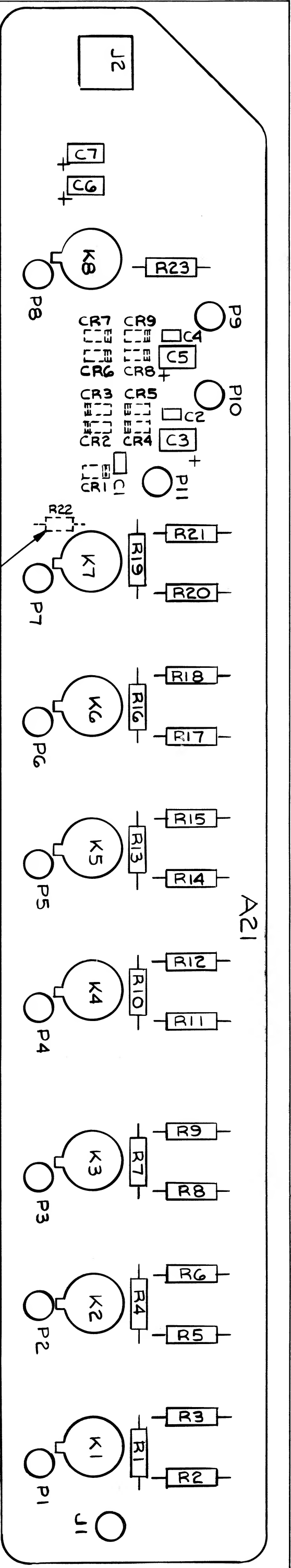


Figure 8-18. A18 Cable Transition PCA

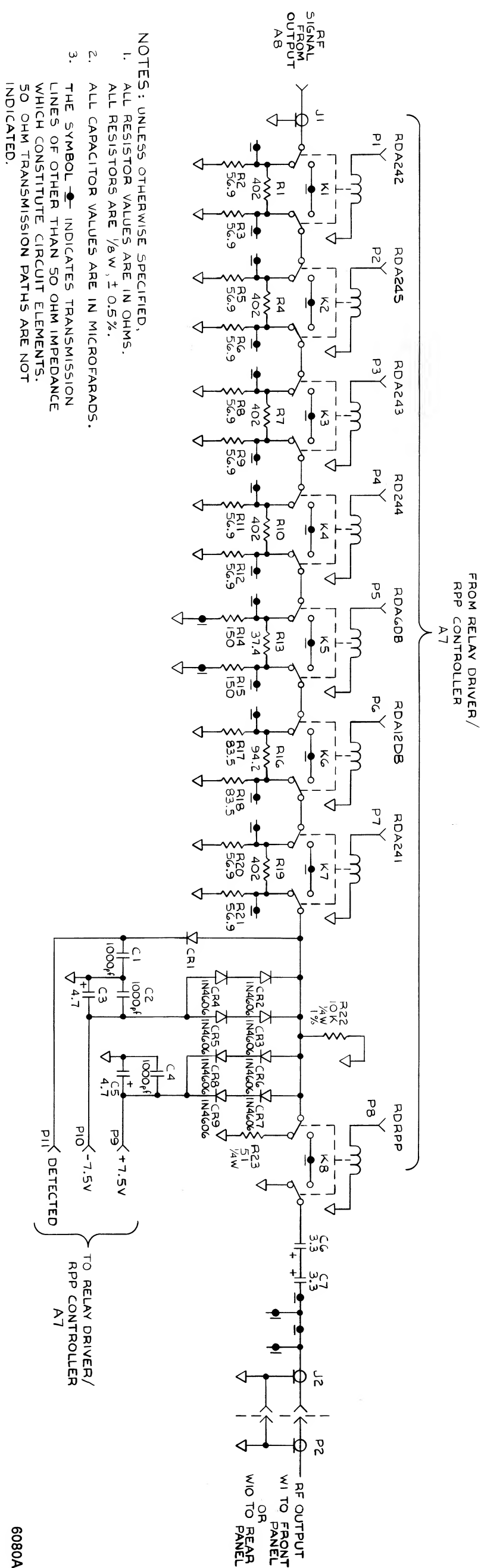


6080A-1051

Figure 8-19. A19 Switch PCA

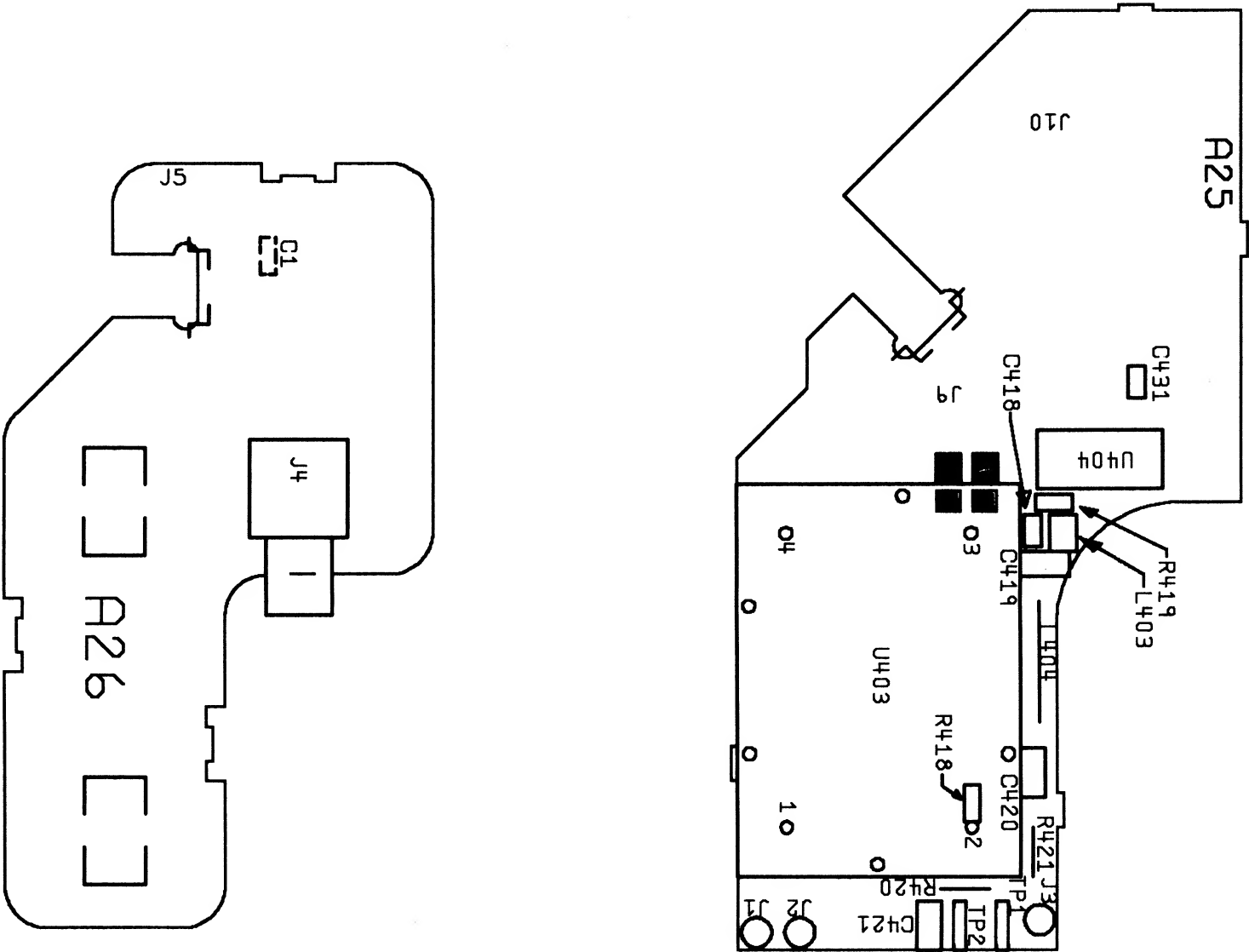


6080A-1636



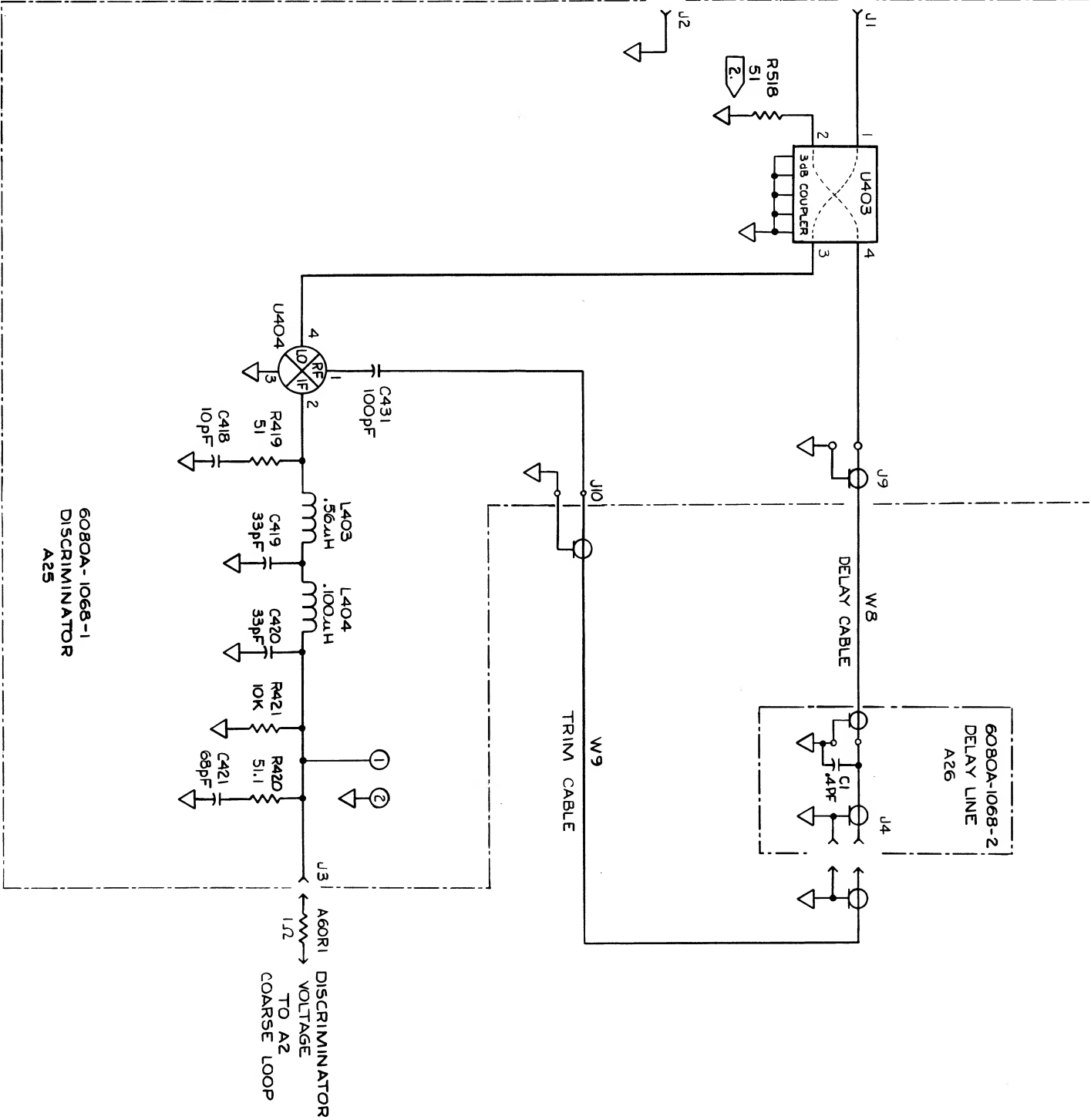
6080A-1036

Figure 8-20. A21 Attenuator PCA



DISCRIMINATOR
RF INPUT
FROM A2
COARSE LOOP

NO CONNECTION
(USED FOR TEST
PURPOSES ONLY.)



NOTES: (UNLESS OTHERWISE SPECIFIED.)

1. ALL RESISTOR VALUES ARE IN OHMS.
2. PARTS INDICATED ARE MOUNTED ON TOP OF THE 3dB COUPLER.
3. "A22" REFERS TO THE ENTIRE DELAY CABLE ASSEMBLY. (BOTH BOARDS AND BOTH CABLES.)

6080A-1668

6080A-1068

Figure 8-21. A22, A25, and A26 Delay
Line/Discriminator PCA